

5250I Emulator



MAPLE

Federal Communication Commission (FCC) Requirements

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions : (1) the device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

NOTE: This equipment has been tested and found to comply with the limits for a CLASS A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

5250I is fully compatible with IBM Enhanced 5250 Emulator card. Users should use a licensed copy of IBM Enhanced 5250 emulation software or IBM Personal communications AS/400 for Windows software to run with 5250I card.

Normally, the IBM Emulation program uses the shared memory block whose location is CC00-CDFF or DC00-DDFF. And the location of the Upper Memory Block (UMB) in DOS are normally defined in A000:0000 to E000:FFFF. To prevent memory conflicts, please add device=C:\DOS\EMM386.EXE X=dc00-ddff or device=C:\Windows\EMM386.EXE X=dc00-ddff in config.sys file if shared memory block located in DC00-DDFF.

Conventions used by switch setting are as follows:

"1"=ON "0"=OFF
 Default I/O Setting is set at 271X
 (Low 10 bits is 31X)

where X is the value A , C, or E

I/O Address	Lower 10 Bits of PC Address	Switch Setting 12345678	I/O Address	Lower 10 Bits of PC Address	Switch Setting 12345678
240X	00X	00001000	253X	13X	11000000
241X	01X	10001000	254X	14X	00100000
242X	02X	01001000	255X	15X	10100000
243X	03X	11001000	256X	16X	01100000
244X	04X	00101000	257X	17X	11100000
245X	05X	10101000	258X	18X	00010000
246X	06X	01101000	259X	19X	10010000
247X	07X	11101000	25AX	1AX	01010000
248X	08X	00011000	25BX	1BX	11010000
249X	09X	10011000	25CX	1CX	00110000
24AX	0AX	01011000	25DX	1DX	10110000
24BX	0BX	11011000	25EX	1EX	01110000
24CX	0CX	00111000	25FX	1FX	11110000
24DX	0DX	10111000	260X	20X	00001100
24EX	0EX	01111000	261X	21X	10001100
24FX	0FX	11111000	262X	22X	01001100
250X	10X	00000000	263X	23X	11001100
251X	11X	10000000	264X	24X	00101100
252X	12X	01000000	265X	25X	10101100

I/O Address	Lower 10 Bits of PC Address	Switch Setting 12345678	I/O Address	Lower 10 Bits of PCAddress	Switch Setting 12345678
266X	26X	01101100	273X	33X	11000100
267X	27X	11101100	274X	34X	00100100
268X	28X	00011100	275X	35X	10100100
269X	29X	10011100	276X	36X	01100100
26AX	2AX	01011100	277X	37X	11100100
26BX	2BX	11011100	278X	38X	00010100
26CX	2CX	00111100	279X	39X	10010100
26DX	2DX	10111100	27AX	3AX	01010100
26EX	2EX	01111100	27BX	3BX	11010100
26FX	2FX	11111100	27CX	3CX	00110100
270X	30X	00000100	27DX	3DX	10110100
271X(default)	31X	10000100	27EX	3EX	01110100
272X	32X	01000100	27FX	3FX	11110100

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