

**DM01 DISK CONTROLLER  
TECHNICAL MANUAL  
(MSCP COMPATIBLE)**



**EMULEX**

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## EMULEX PRODUCT/MANUAL REVISION HISTORY

### PROM E52x<sup>1</sup>, Location U46

PROM E52x	DESCRIPTION	MANUAL P/N
A, B, or C	DM01 with optional diagnostics	DM0151001-00
D and above	DM01 with firmware-resident diagnostics	DM0151002-00

This manual has been extensively revised to incorporate changes to support the Firmware-Resident Diagnostics (F.R.D.) that have been added to the Revision D controller firmware PROM. Due to the nature of these firmware changes, a DM01 with a Revision D and above firmware PROM will no longer operate with previously supplied diagnostic software. In addition, some of the ODT functions (NOVRAM loading commands and Format Drive command) previously available are no longer available.

All of the functionality that was provided by software diagnostics and ODT commands has been incorporated in F.R.D. Be certain that your manual is appropriate for the revision level of your controller firmware, as noted in the table above. This firmware is easily identified by the label at U46.

### WARNING

This equipment generates, uses and can radiate radio frequency energy, and if not installed and used in accordance with the technical manual, may cause interference to radio communications. It has been tested and found to comply with the limits for a Class A computing device pursuant to Subpart J of Part 15 of Federal Communications Commission (FCC) Rules, which are designed to provide reasonable protection against such interference when operating in a commercial environment. Operation of this equipment in a residential area is likely to cause interference in which case the user at his own expense will be required to take whatever measures may be required to correct the interference.

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<sup>1</sup> The small x indicates the PROM's revision level letter: A, B, C, etc.

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## EMULEX PRODUCT WARRANTY

**CONTROLLER WARRANTY:** Emulex warrants for a period of twelve (12) months from the date of shipment that each Emulex controller product supplied shall be free from defects in material and workmanship.

**CABLE WARRANTY:** All Emulex provided cables are warranted for ninety (90) days from the time of shipment.

The above warranties shall not apply to expendable components such as fuses, bulbs, and the like, nor to connectors, adaptors, and other items not a part of the basic product. Emulex shall have no obligation to make repairs or to cause replacement required through normal wear and tear or necessitated in whole or in part by catastrophe, fault or negligence of the user, improper or unauthorized use of the product, or use of the product in such a manner for which it was not designed, or by causes external to the product, such as but not limited to, power failure or air conditioning. Emulex's sole obligation hereunder shall be to repair or replace any defective product, and, unless otherwise stated, pay return transportation cost for such replacement.

Purchaser shall provide labor for removal of the defective product, shipping charges for return to Emulex and installation of its replacement. THE EXPRESSED WARRANTIES SET FORTH IN THIS AGREEMENT ARE IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED OR IMPLIED, INCLUDING WITHOUT LIMITATION, ANY WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, AND ALL OTHER WARRANTIES ARE HEREBY DISCLAIMED AND EXCLUDED BY EMULEX. THE STATED EXPRESS WARRANTIES ARE IN LIEU OF ALL OBLIGATIONS OR LIABILITIES ON THE PART OF EMULEX FOR DAMAGES, INCLUDING BUT NOT LIMITED TO SPECIAL, INDIRECT, OR CONSEQUENTIAL DAMAGES ARISING OUT OF, OR IN CONNECTION WITH THE USE OR PERFORMANCE OF THE PRODUCT.

**RETURNED MATERIAL:** Warranty claims must be received by Emulex within the applicable warranty period. A replaced product, or part thereof, shall become the property of Emulex and shall be returned to Emulex at Purchaser's expense. All returned material must be accompanied by a RETURN MATERIALS AUTHORIZATION (RMA) number assigned by Emulex.

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## **1.1 Overview**

---

The DM01 Disk Controller, designed and manufactured by Emulex Corporation, is a Mass Storage Control Protocol (MSCP) compatible controller with a Seagate ST-506 hard disk interface and Shugart SA450 floppy disk interface. This manual is designed to help you install and use your DM01 Disk Controller. It assumes that you have some knowledge of hardware configuration, LSI-11 architecture and terminology, and interpretations of error messages and device register contents. The contents of the eight sections and three appendices are described as follows:

- **Section 1 (General Description):** This section contains an overview of the DM01 Disk Controller.
- **Section 2 (Controller Specification):** This section contains the specification for the DM01 Disk Controller.
- **Section 3 (Planning the Installation):** This section contains the information necessary to plan your installation, including MSCP subsystem and operating system considerations.
- **Section 4 (Installation):** This section contains the information needed to set up and physically install the controller, including switch settings and cabling. It also describes the firmware-resident diagnostics and contains instructions for loading drive configuration parameters into the NOVRAM.
- **Section 5 (Troubleshooting):** This section describes fault isolation procedures that can be used to pinpoint trouble spots.
- **Section 6 (Registers and Programming):** This section describes the DM01's LSI-11 bus registers and presents an overview of the Mass Storage Control Protocol (MSCP).
- **Section 7 (Functional Description):** This section describes the controller architecture.
- **Section 8 (Interfaces):** This section describes the controller LSI-11 bus, ST-506 disk interface, and SA450 floppy interface.
- **Appendix A (Autoconfigure):** This appendix describes the DEC algorithm for the assignment of CSR addresses and vector addresses.

- **Appendix B (PROM Removal and Replacement):** This appendix contains instructions to remove and replace the firmware so that the user can upgrade the DM01 Disk Controller in the field.
- **Appendix C (Disk Drive Configuration Parameters):** This appendix contains configuration parameters for supported ST-506 disk drives and SA450 floppy drives.

---

## **1.2 Subsystem Overview**

---

The DM01 Disk Controller connects high-capacity mass storage peripherals to the LSI-11 bus on computers manufactured by Digital Equipment Corporation (DEC). The DM01 implements DEC's Mass Storage Control Protocol (MSCP) to provide a software-transparent interface for the host DEC computer. To provide traditional Emulex flexibility in peripheral selection, the DM01 uses the industry standard ST-506 hard disk interface and the SA450 as its floppy disk interface. The media used with the floppy disk drive are compatible with DEC RX50 media.

---

### **1.2.1 Mass Storage Control Protocol (MSCP)**

---

MSCP is a software interface designed to lower the host computer's mass storage overhead by offloading much of the work associated with file management into an intelligent mass storage subsystem. In concert with ST-506 compatible peripherals, the DM01 provides just such a subsystem. The DM01 relieves the host CPU of many file maintenance tasks. The DM01 Disk Controller performs these MSCP functions: error checking and correction, bad block replacement, seek optimization, command prioritizing and ordering, and data mapping.

This last feature is, perhaps, the most important. This feature allows the host computer's operating system software to store data in logical blocks that are identified by simple logical block numbers (LBNs). Thus, the host does not need to have detailed knowledge of the peripheral's geometry (cylinders, tracks, sectors, etc.). This feature also makes autoconfiguration a simple matter. During system start-up, the host operating system queries the subsystem to find its capacity (the number of logical blocks that the subsystem can store).

Because the host operating system does not need to have detailed knowledge of its mass storage subsystem, the complexity of the operating system itself has been reduced. This reduction comes about because only one or two software modules are required to allow many different subsystems to be connected to a host.

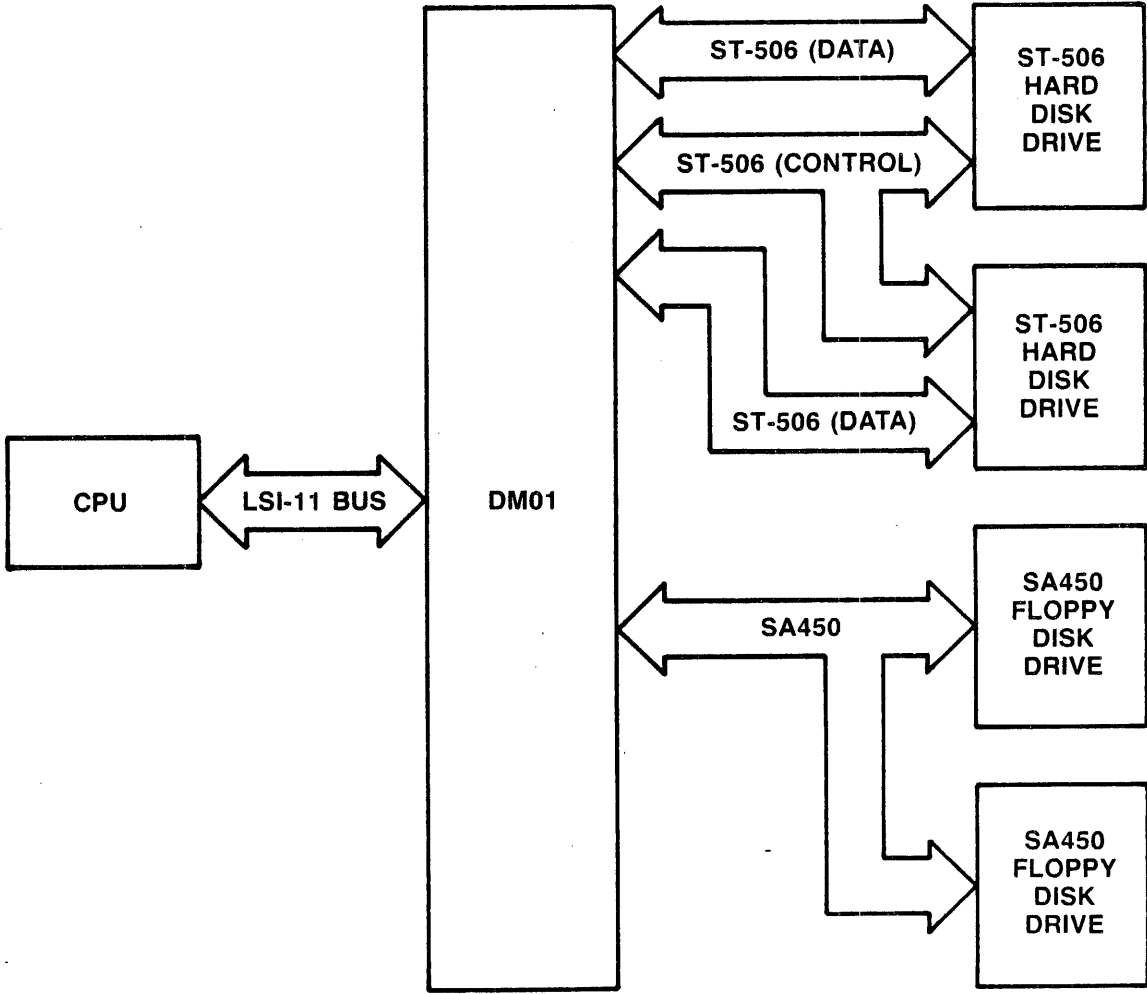
### 1.3 Physical Organization Overview

The DM01 Disk Controller is a modular, microprocessor-based disk controller that connects directly to the host computer's LSI-11 bus backplane. The microprocessor architecture ensures excellent reliability and compactness.

The DM01 is contained on a single dual-wide printed circuit board assembly (PCBA) that plugs directly into a LSI-11 bus backplane slot.

The DM01 supports a maximum of two physical hard disk drives and two physical floppy disk drives. In addition, the DM01 NOVRAM is capable of splitting each hard disk into two logical units. Under RT-11 and MicroVMS, the DM01 supports a maximum of six logical drives; under RSX-11M, RSX-11M-PLUS, and RSTS/E, the DM01 supports a maximum of four logical drives.

Figure 1-1 shows one possible physical subsystem configuration.



DM0102-0916A

Figure 1-1. DM01 Subsystem Configuration

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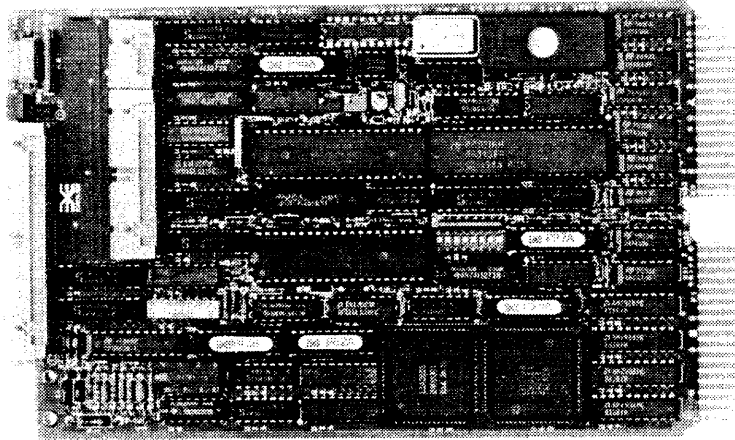
## 1.4 Subsystem Components

---

The DM01 Disk Controller, with appropriate peripherals, provides a DEC MSCP-compatible mass storage subsystem. The DM01 is pictured in Figure 1-2. The DM01 is identified by a top level assembly tag that is glued to the 8031 microprocessor chip on the PWB. The DM01 top level assembly number is given in Table 1-1 along with the part numbers of the items that are delivered with the DM01.

Table 1-1. Basic Contents

Item	Qty	Description	Part Number
1	1	DM01 Disk Controller	DM0110201-02
2	1	22-Bit Addressing Kit	QD0113002-00
3	1	DM01 Technical Manual	DM0151002-00



DM0102-0917

Figure 1-2. DM01 Disk Controller

Table 1-2 lists the options that can be ordered to tailor your DM01 to your particular application.

Table 1-2. Subsystem Options

Option	Description
QD0113001-00	DM01 Internal Cabling Kit. It includes: 50-line, flat ST-506 interface cable, 3 ft. 34-line, flat daisy-chain cable, 1 ft. 34-line, flat SA450 interface cable, 3 ft.

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## 1.5 Features

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The following features enhance the usefulness of the DM01 Disk Controller.

---

### 1.5.1 Microprocessor Design

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The DM01 design incorporates an eight-bit, high-performance CMOS microprocessor to perform all controller functions. The microprocessor approach provides a reduced component count, high reliability, easy maintainability, and the microprogramming flexibility that allows MSCP to be implemented without expensive, dedicated hardware.

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### 1.5.2 Firmware-resident Diagnostics

---

The DM01 disk controller firmware incorporates a self-contained set of disk preparation and diagnostic utilities. These utilities are contained in DM01 Revision D and above firmware. Controllers with this firmware are easily identified by a tag labeled with the top assembly number DM0110201-00E.

These utilities allow the user to communicate directly with the DM01 via a firmware resident terminal driver that is compatible with either CRT or hardcopy devices connected to an LSI-11 or MicroVAX console port. These firmware-resident diagnostics (F.R.D.), also known as onboard diagnostics, provide several important disk preparation functions, including the ability to:

- Configure the controller NOVRAM
- Format the drive
- Test the disk surface and replace defective blocks, and
- Perform reliability testing of the attached disk subsystem.

---

**1.5.3 Custom Configuration Capability**

---

An onboard NOVDRAM can be programmed for four independent physical drive configurations: two ST-506 drives and two floppy drives. Using the firmware-resident utilities, you can control drive parameters such as gap size and the number of sectors per track of the attached ST-506 drives for custom formats.

---

**1.5.4 Self-test**

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The DM01 incorporates an internal self-test routine which exercises all parts of the microprocessor, the onboard memory, the LSI-11 bus interface, and the ST-506 interface. Although this test does not completely test all circuitry, successful execution indicates a very high probability that the disk controller is operational. If the DM01 detects an error during self-test, it leaves three light-emitting diodes (LEDs) ON and sets an error bit in the Status and Address (SA) register (base address plus two).

---

**1.5.5 Error Control**

---

The disk controller presents error-free media to the operating system by correcting soft errors and retrying operations without intervention by the host. Errors are reported but not replaced on floppy disks.

---

**1.5.6 Host-Initiated Bad Block Replacement**

---

The DM01 uses DEC-compatible host initiated bad block replacement to dynamically replace any defective blocks that occur during the life of the system. For maximum reliability, the DM01 reports even corrected single bit errors as candidates for replacement. This feature is not supported on floppy drives.

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**1.5.7 Media Defect List Management**

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The media defect list must be manually entered.

---

**1.5.8 Seek Optimization**

---

The DM01 is able to pool the various seeks that need to be performed and determine the most efficient order in which to do them. This is an especially important feature in heavily loaded systems. The disk controller's ability to arrange seeks in the optimal order saves a great deal of time and makes the entire system more efficient.



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### **1.5.9 Command Buffer**

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The DM01 contains a buffer that is able to store 13 MSCP commands. This large buffer allows the subsystem to achieve a higher throughput and to operate at a very efficient level.

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### **1.5.10 Adaptive DMA**

---

During each DMA data transfer burst, the DM01 monitors the LSI-11 bus for other pending DMA requests and suspends its own DMA activity to permit other DMA transfers to occur. The host processor programs the DMA burst length during the MSCP initialization sequence or the DM01 defaults to 16 words per burst. Because of these adaptive DMA techniques, the DM01 ensures that CPU functions, including interrupt servicing, are not locked out for excessive periods of time by high-speed disk transfers. The DM01 firmware design includes a DMA burst delay of either 4 or 8 microseconds to avoid data-late conditions.

In addition, the DM01 allows you to modify its DMA operations by disabling the adaptive DMA and defaulting to burst transfers of 8 words or less.

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### **1.5.11 Block-mode DMA**

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The DM01 supports block-mode DMA for accessing memory. In this mode, the initial address of the data is transmitted, followed by a burst of up to 16 words of data. The memory address is automatically incremented to accommodate this burst. Block mode transfers considerably reduce the overhead associated with DMA operations.

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### **1.5.12 22-Bit Addressing**

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The DM01 supports the 22-bit addressing capability of the extended LSI-11 bus.

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## **1.6 Compatibility**

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This subsection describes the compatibility of operating systems and hardware.

---

### 1.6.1 Operating Systems

---

The DM01 implements MSCP. Emulex supports its implementation of MSCP beginning with the indicated version of the following DEC operating systems:

Operating System	Version
Micro/VMS	4.0
RSTS/E	8.0
RSX-11M	4.1
RSX-11M-PLUS	2.1
RT-11	5.1
Ultrix-11	3.0
Ultrix-32	1.1

---

### 1.6.2 Hardware

---

The DM01 Disk Controller complies with DEC LSI-11 bus protocol, and it directly supports 22-bit addressing and block-mode DMA. The DM01 also supports scatter-write and gather-read operations on the MicroVAX I.

The DM01 supports hard disk drives that conform to the ST-506 interface and is certified to support the following SA450 floppy disk drives:

- NEC FD1055
- Panasonic JU-465-5
- Tandon TM65-4
- TEAC FD-55F
- TEAC FD-55FV

The hard disk drives supported by the DM01 are not media compatible with comparable DEC MSCP products; this is not a problem due to the fixed nature of most DEC disk drives.

The floppy disk drives supported by the DM01 are media compatible with DEC RX50 floppy disk drives.

---

### 1.6.3 Pack Transition Support

---

Pack transition notification alerts the system when new media are inserted in a drive. DM01 pack transition support minimizes the chance of the system writing to a changed floppy as if it were the original diskette.

An SA450 drive may have several, jumper-selectable definitions of READY line (34). To support pack transition, the SA450 READY line must be TRUE as a function of the floppy door being closed.

There are some operating system limitations. RSX-11M and RSTS/E require pack transition support. RSX-11M-PLUS and MicroVMS do not require pack transition support, but the user should exercise caution when changing floppies. The pack transition concept does not apply to RT-11.



## 2.1 Overview

This section contains the general, environmental, physical, and electrical specifications for the DM01 Disk Controller.

Subsection	Title
2.1	Overview
2.2	General Specification
2.3	Environmental Specification
2.4	Physical Specification
2.5	Electrical Specification

## 2.2 General Specification

Table 2-1 contains a general specification for the DM01 Disk Controller.

Table 2-1. DM01 General Specifications

Parameter	Description														
<b>FUNCTION</b>	Providing mass data storage to Digital Equipment Corporation (DEC) computers that use the LSI-11 bus														
<b>Logical CPU Interface</b>	Emulates DEC's Mass Storage Control Protocol (MSCP)														
Diagnostics	Embedded diagnostics														
Operating System Compatibility	<table style="width: 100%; border: none;"> <tr> <td style="width: 60%;">Micro/VMS</td> <td>V4.0 and above</td> </tr> <tr> <td>RSTS/E</td> <td>V8.0 and above</td> </tr> <tr> <td>RSX-11M</td> <td>V4.1 and above</td> </tr> <tr> <td>RSX-11M PLUS</td> <td>V2.1 and above</td> </tr> <tr> <td>RT-11</td> <td>V5.1 and above</td> </tr> <tr> <td>Ultrix-11</td> <td>V3.0 and above</td> </tr> <tr> <td>Ultrix-32m</td> <td>V1.1 and above</td> </tr> </table>	Micro/VMS	V4.0 and above	RSTS/E	V8.0 and above	RSX-11M	V4.1 and above	RSX-11M PLUS	V2.1 and above	RT-11	V5.1 and above	Ultrix-11	V3.0 and above	Ultrix-32m	V1.1 and above
Micro/VMS	V4.0 and above														
RSTS/E	V8.0 and above														
RSX-11M	V4.1 and above														
RSX-11M PLUS	V2.1 and above														
RT-11	V5.1 and above														
Ultrix-11	V3.0 and above														
Ultrix-32m	V1.1 and above														

*(continued on next page)*

Table 2-1. DM01 General Specifications (continued)

Parameter	Description
<b>CPU I/O Technique</b>	Direct Memory Access (DMA), including adaptive techniques and block mode; supports scatter-write and gather-read operations on the MicroVAX I
<b>INTERFACE</b>	
<b>CPU Interface</b>	Extended LSI-11 bus interface
Device Base Address	Standard 17772150 <sub>8</sub> Alternates 17772154 <sub>8</sub> 17760334 <sub>8</sub> 17760340 <sub>8</sub> 17760344 <sub>8</sub> 17760350 <sub>8</sub> 17760354 <sub>8</sub> 17760360 <sub>8</sub>
Vector Address	Programmable
Priority Level	BR4
Bus Loading	1 dc Load, 2.5 ac Loads
<b>Hard Disk Interface</b>	Seagate Technology ST-506
Number of Physical Disk Drives Supported	2
Disk Transfer Rate	500K bytes per second
Maximum Cable Length	20 ft. (6 m.)
<b>Floppy Disk Interface</b>	Shugart SA450
Number of Physical Disk Drives Supported	2
Data Transfer Rate	250K bits per second
Maximum Cable Length	10 ft. (3 m.)

(continued on next page)

Table 2-1. DM01 General Specifications (continued)

<b>Parameter</b>	<b>Description</b>
<b>Firmware Diagnostic Access Path</b>	
LSI-11	Standard console terminal
MicroVAX	Standard console terminal (except GPX workstation)

### **2.3 Environmental Specification**

Table 2-2 contains the environmental specifications for the DM01 Disk Controller.

Table 2-2. DM01 Environmental Specifications

<b>Parameter</b>	<b>Description</b>
<b>OPERATING TEMPERATURE</b>	10°C (50°F) to 40°C (104°F), where maximum temperature is reduced 1.8°C per 1000 meters (1°F per 1000 feet) altitude
<b>RELATIVE HUMIDITY</b>	10% to 90% with a maximum wet bulb of 28°C (82°F) and a minimum dewpoint of 2°C (3.6°F)
<b>COOLING</b>	6 cubic feet per minute
<b>HEAT DISSIPATION</b>	82 BTU per hour

---

## **2.4 Physical Specification**

---

Table 2-3 contains the physical specifications for the DM01 Disk Controller.

Table 2-3. DM01 Physical Specifications

<b>Parameter</b>	<b>Description</b>
<b>PACKAGING</b>	Single, dual-wide, four-layer PCBA
<b>Dimensions</b>	5.186 by 8.70 inches 13.172 by 22.09 centimeters (see Figure 2-1)
<b>Shipping Weight</b>	3 pounds

---

## **2.5 Electrical Specification**

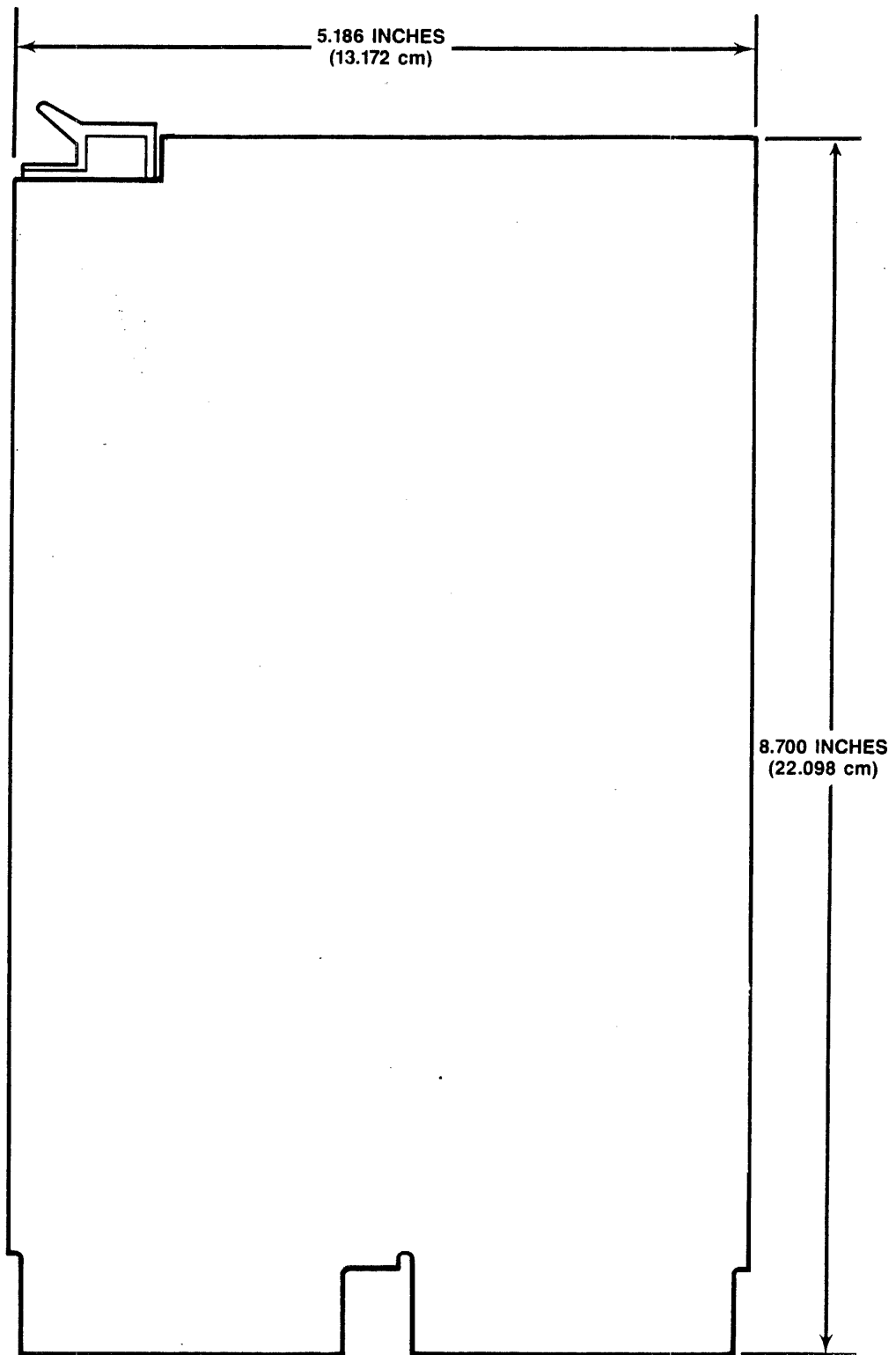
---

Table 2-4 lists and describes the electrical specification for the DM01 Disk Controller.

Table 2-4. DM01 Electrical Specification

<b>Parameter</b>	<b>Description</b>
<b>POWER</b>	+5 Vdc $\pm$ 5%, 2.6 amperes (A) MAX





DM0102-0634



---

## **3.1 Overview**

---

This section is designed to help you plan the installation of your DM01 Disk Controller. Taking a few minutes and planning the configuration of your subsystem before beginning its installation should result in a smoother installation with less system down time. This section contains DM01 application examples and configuration procedures. The subsections are listed in the following table:

Subsection	Title
3.2	MSCP Subsystem Configuration
3.3	A DEC MSCP Subsystem
3.4	The DM01 MSCP Subsystem
3.5	Operating Systems, Device and Vector Addresses

---

## **3.2 MSCP Subsystem Configuration**

---

The following paragraphs describe MSCP Subsystem concepts, including architecture, unit numbering, capacities, and related concepts.

---

### **3.2.1 Architecture**

---

MSCP is a protocol designed by DEC for mass storage subsystems using Digital Storage Architecture (DSA). In a MSCP mass storage subsystem, DSA comprises three functional and physical layers:

- **Host Layer.** An MSCP class-driver in the host system receives requests from the operating system and then relays data and commands to the controller in MSCP message packets.
- **Controller Layer.** The MSCP controller communicates with both the host layer and the mass storage layer. The controller transmits MSCP message packets to and from the host MSCP class-driver and performs data-handling functions for the mass storage devices. The DM01 functions as the controller layer.
- **Mass Storage Layer.** The mass storage peripheral devices communicate with the MSCP controller and send or receive data as specified by the MSCP controller.

MSCP defines the form of the message packets that are exchanged by the host and the MSCP controller. The DM01 implements MSCP in mass storage subsystems using ST-506 as the hard disk drive interface and SA450 as the floppy disk drive interface.

---

### **3.2.2 Peripheral Numbering**

---

Each MSCP peripheral on the system is identified to the operating system by an MSCP device name. An MSCP device name consists of a device class identifier and a unit number. The device class is indicated by a two-letter prefix; MSCP disk devices are indicated by the prefix DU.

With the exception of MicroVMS systems, DEC operating systems require that all MSCP peripherals on a system have different MSCP device numbers, even if they are managed by separate MSCP controllers at separate LSI-11 bus device addresses. For example, under RSX-11M-PLUS, if there are three peripherals on the first MSCP controller (at 772150<sub>g</sub>), then the first peripheral on the second MSCP controller (in floating CSR address space) is numbered DU3.

---

### **3.2.3 Peripheral Capacities**

---

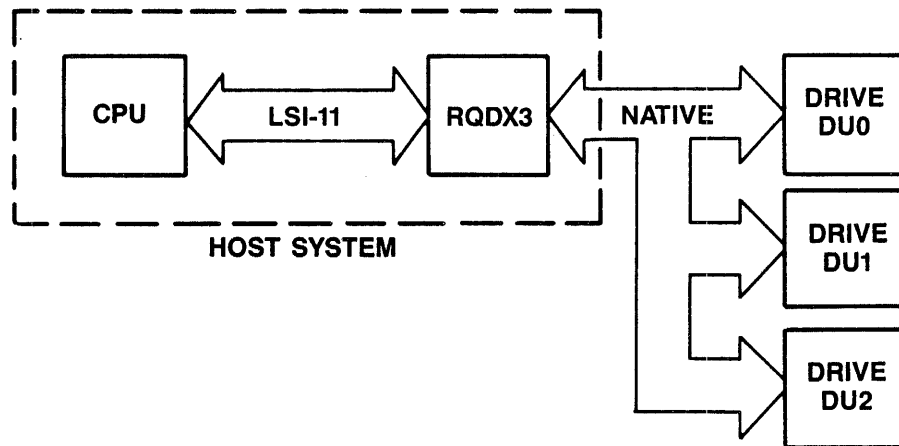
The capacity of peripherals in an MSCP subsystem is measured in logical blocks. Each logical block contains 512 bytes of data. The MSCP controller can report the capacity of a peripheral to the operating system. A 10M byte peripheral such as DEC's RD51 is able to store about 20,000 logical blocks.

---

## **3.3 A DEC MSCP Subsystem**

---

Figure 3-1 shows the organization of a typical DEC MSCP subsystem for the LSI-11 bus. The MSCP host and controller functions are combined in a single piece of hardware, in this example the DEC RQDX3. The RQDX3 supports RD51, RD52, or RD53 hard disk drives and the RX50 5.25-inch floppy drive. The RQDX3 plugs directly into the LSI-11 bus and is attached to disk drives via a disk-drive-native interface.

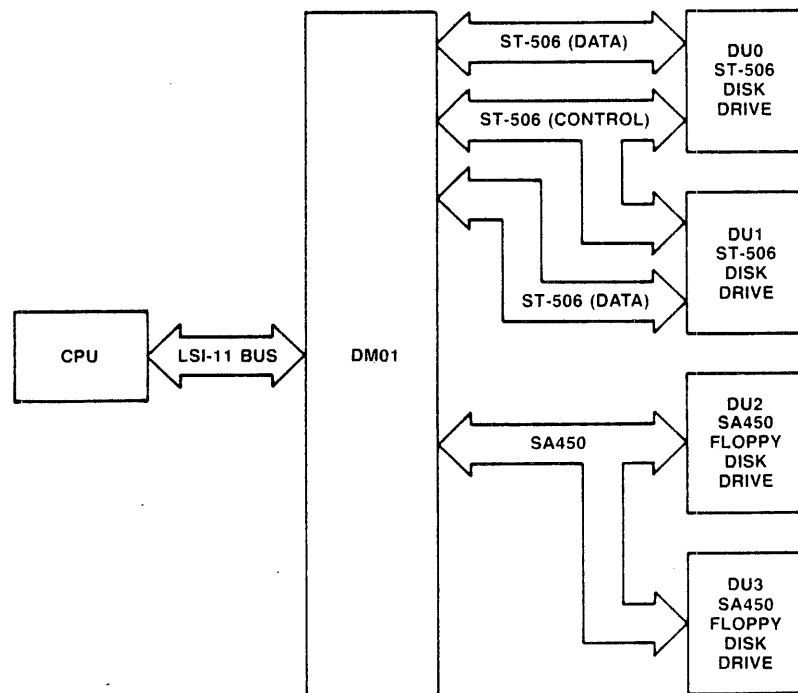


DM0102-0555

Figure 3-1. DEC MSCP Subsystem Logical and Physical Configuration

### 3.4 The DM01 Subsystem

Figure 3-2 illustrates a typical DM01 MSCP subsystem. As with the DEC implementation, the DM01 is connected directly to the LSI-11 bus. The DM01, however, uses the ST-506 peripheral interface to communicate with one or two hard disk drives and the SA450 interface to communicate with one or two floppy disk drives.



DM0102-0917A

Figure 3-2. DM01 Subsystem Logical and Physical Configuration

The MSCP subsystem provided by the DM01 is essentially analogous to the DEC MSCP subsystem. As in the DEC subsystem, the DM01 MSCP controller connects directly to the LSI-11 bus. As an MSCP controller, the DM01 receives requests from the host, optimizes the requests, generates ST-506 and SA450 commands to perform the operations, transfers data to and from the host, transfers data to and from the device, and buffers data as necessary. When the command is complete, the controller sends a response to the host.

The DM01 also performs all of the functions of the peripheral controller, including serialization and deserialization of data. The DM01 connects to the hard disk peripherals it supports via the ST-506 interface and to the floppy disk peripherals via the SA450 interface.

---

### 3.4.1 Logical Unit Numbers

---

As noted in section 3.2.2, most DEC operating systems do not allow any MSCP disk devices on one system to have the same unit number, even though they may be controlled by separate MSCP controllers at different base addresses.

DEC MSCP-type drives can accept unit identification plugs that define addresses from 0 to 255. Disk drives controlled by the DM01 do not have this flexibility; the DM01 can detect only two unique drive addresses at its peripheral interfaces -- 0 and 1. To prevent a unit-number conflict between the DM01's drives and another MSCP controller's drives, the DM01 employs switches to change the drive logical unit number that is reported to the operating system.

*Example 3-1: An MSCP controller at a standard base address supports four disk drives; a DM01 at an alternate base address supports two disk drives. An offset of 4 is specified for the DM01. This causes the DM01 ST-506 disk with address 0 to be reported to the operating system as logical unit number (LUN) 4. The DM01 disk 2 is reported as LUN 5.*

The offset for the logical unit number is specified by using switches SW1-2 through SW1-4 on the DM01.

---

### 3.4.2 DM01 Subsystem Logical Configuration

---

This subsection explains the algorithm used by the DM01 to map logical MSCP peripherals onto the physical hard disk drives provided by the DM01 subsystem. Note that the floppy drives may not be divided into multiple logical units.

---

### 3.4.2.1 Logical Devices

---

The phrase "logical MSCP disk drive" refers to the disk drive as it appears to the operating system. That is, the operating system associates a disk drive of known type (in this case, an MSCP disk drive) with a unit number and a capacity. The DM01 MSCP controller presents that information to the operating system after initialization on command.

Because the MSCP controller is responsible for establishing the relationship between unit number and capacity, it is possible for the controller to divide its physical disk drives into more than one logical unit. Each part is then assigned a separate unit number, and the unit number and capacity of each part is presented to the operating system.

The operating system then sees the two parts as separate disk drives, even though the data is actually stored on the same physical drive. The two parts are called logical disk drives, and the numbers that identify them are called MSCP unit numbers.

A drive configuration that supports multiple logical units is specified by the data that is stored in the configuration Nonvolatile Random Access Memory (NOVRAM). Information for programming the configuration NOVRAM is given in subsections (4.8, 4.9, and 4.10). The field that causes a drive to be divided into multiple logical units is called the Split Code. There are three types of split codes: no split, cylinder split, and head split:

- When no split is specified, the entire physical drive is one logical drive.
- Cylinder split codes divide a physical drive by cylinders. A Starting Cylinder Offset field in the NOVRAM specifies the first cylinder of the second logical drive. Alternate cylinders are divided evenly between drives. For example, if the physical drive has 645 cylinders and is split 50/50, the first 322 cylinders are assigned to one drive and the second 322 cylinders are assigned to the other (the odd cylinder is not used).

- Head split codes divide the drive by data heads. A Starting Head Offset field in the NOVRAM specifies the first head of the second logical drive. When the drive is split by data heads, each logical drive has its own platter(s); consequently, the lower blocks of one logical drive are in the same physical cylinder as the lower blocks of the other logical drive. For example, a Maxtor drive with 15 heads might be divided so that the first logical unit is assigned heads 0 through 6, and the second logical unit is assigned heads 7 through 14. The Starting Head Offset has a value of 7.

The head splitting technique has a performance advantage over the cylinder method. Typically, most disk accesses are made in the lower cylinders of a disk because many system-oriented files are located there, including the drive's directory. Because the low (and high) cylinders are vertically aligned between the two logical drives when the head splitting technique is used, switching between head-split logical drives requires less head movement than switching between cylinder-split drives.

---

### **3.4.2.2 Device Numbers**

---

The drives supported by the DM01 are assigned MSCP device names by the operating system. As described in subsection 3.2.2, an MSCP device name consists of a device class prefix and a device unit number. Drives are assigned MSCP device numbers beginning with zero (0). The conventions for numbering multiple MSCP drives vary by operating system.

Under RSX-11M, RSX-11M-PLUS and RT-11, DU0 is assigned to the first drive on the first MSCP controller, where "first" means the controller located at the standard base address. Unit number 1 would be the second drive on the first controller, etc. If there are two MSCP controllers on the system, the units installed on the second begin numbering at  $n + 1$ , where  $n$  equals the highest unit number of the first MSCP controller.

RSTS/E requires that drives supported by a standard MSCP controller be numbered starting at 0 and drives supported by an alternate MSCP controller be numbered starting at 4.



Because MSCP device names under MicroVMS designate the supporting MSCP controller, the unit numbering is less restricted. For example, two drives which are supported by a standard MSCP controller might be DUA0 and DUA1 and a third drive supported by an alternate MSCP controller might be DUB0.

Table 3-1 is an MSCP unit numbering example under the RSX-11M operating system which shows the MSCP number versus the actual physical addresses assigned to all the components. The physical disk drive (unit number 1) of the second controller is split into two logical units. Note that two device names are associated with that drive.

Table 3-1. Subsystem Configuration Example

DM01 Address	Device Description	Drive Unit Number	Device Name
772150	Atasi 3046	0	DU0
	Atasi 3046	1	DU1
760334 (Floating)	TEAC FD-55F	0	DU2
	TEAC FD-55F	1	DU3

**NOTE**

The device identifier for hard disk drives supported by the DM01 is RA81; the device identifier for floppy disk drives supported by the DM01 is RX50.

---

### 3.5 Operating Systems, Device and Vector Addresses

---

Before the installation of any peripheral device can be considered complete, the computer's operating system must be made aware of the new resource. The information provided in this section is intended to supplement your DEC operating system resources and to be used as an aid in planning the installation of your DM01.

An operating system can be made aware of a new resource in three ways:

- The operating system can poll the computer's I/O device address space.
- The device can be manually connected using CONNECT or CONFIGURE statements.
- The user can tell the operating system about a device during an interactive SYSGEN procedure.

The first technique is referred to as autoconfigure, and it is essentially automatic. The second technique requires that CONNECT statements be placed in a special command file that is executed each time the computer is bootstrapped. The third technique, interactive SYSGEN, creates a configuration file that the operating system references when the system is bootstrapped. All techniques accomplish the same result: they associate a specific device type with a bus address and interrupt vector.

Most recent versions of DEC operating systems use autoconfigure to some extent, and try to follow the same rules. The RT11 operating system does not use autoconfigure, but can locate devices that reside at a standard address. There are some differences among operating systems, however, especially with regard to MSCP controllers at alternate LSI-11 bus addresses. The following paragraphs address these differences for each supported operating system. This discussion includes information on choosing appropriate LSI-11 bus device addresses and interrupt vectors for the subsystem.

The following operating system discussions give procedures for choosing LSI-11 bus addresses for the first MSCP controller and any subsequent controllers in the host configuration. No instructions are provided for programming the chosen address into the DM01.

MSCP-type controllers contain two registers that are visible to the LSI-11 bus I/O page. They are the Initialization and Polling (IP) register (base address) and the Status and Address (SA) register (base address plus 2). The IP register, the CSR address, LSI-11 bus address and the base address all refer to the same register. All of the operating systems described in the following subsections use the standard LSI-11 bus address of  $1772150_8$  for the first controller on the host system.

Vector addresses for MSCP controllers are not selected by using switches on the controller, but are programmed into the controller during the Initialization process. Many operating systems select the vector address automatically. If an operating system requires manual input of the vector, the procedure notes that fact.

Again, although DEC has attempted to standardize treatment of peripherals by operating systems, some differences do exist. Table 3-2 lists and describes the device names assigned to MSCP devices under five operating systems. Two controller names and two drive names are given to indicate the numbering scheme.

Table 3-2. Device Names

Operating System	Controller First, Second	Drives Supported by First Controller
RSTS/E	RU0, RU1	DU0, DU1
RSX-11M	--- ---	DU0, DU1
RSX-11MPLUS	DUA, DUB	DU0, DU1
RT-11	Port0, Port1	DU0, DU1
MicroVMS	PUA, PUB	DUA0, DUA1
Ultrix-11	uda0, uda1	ra0, ra1
Ultrix-32m	uda0, uda1	ra0, ra1

The information regarding operating systems in these subsections is subject to change. The following discussions are based on three assumptions:

- This is the first pass that is being made through SYSGEN; therefore, no saved answer file exists. Answer N (no) to questions such as "Use as input saved answer file?"
- Your host system configuration conforms to the standard LSI-11 device configuration algorithm (otherwise autoconfigure results are not reliable).
- You are generating a mapped version of the operating system on the appropriate hardware (unless you are using RT11).

### 3.5.1 RSTS/E Operating Systems (V8.0 and above)

RSTS/E scans the hardware to determine configuration each time the system is bootstrapped. The scanning program is called INIT.SYS and it relies on the same hardware configuration conventions as do the other DEC operating systems.

The RSTS/E operating system can support two MSCP controllers. The first MSCP controller must be located at the standard LSI-11 bus address, 772150<sub>8</sub>. According to DEC documentation, the second unit should be located in floating address space. For an alternate DM01, Emulex suggests specifying a LSI-11 bus address of 760334<sub>8</sub> using the HARDWARE option of the INIT.SYS program.

The INIT.SYS program uses a user-specified table, located in the currently installed monitor, to make exceptions to the autoconfigure algorithm. This table is modified by the HARDWARE option of the INIT.SYS program. Use of this table allows an MSCP controller to be placed at virtually any address on the I/O page. Note that this table must be reset any time a new monitor is installed. Emulex suggests using an LSI-11 bus address of 760334<sub>8</sub> for an alternate DM01. An MSCP controller must be located at the standard address to be a bootstrap device.

Interrupt vector addresses are assigned to the MSCP controllers by INIT.SYS and programmed into the devices during initialization.

---

### **3.5.1.1 Adding MSCP Support**

---

Support for an MSCP controller must be included in a monitor at SYSGEN time. To include support for an MSCP controller in a RSTS/E monitor, respond to the SYSGEN question "number of MSCP controllers" with the number of MSCP controllers on the system.

Units connected to MSCP controllers will be accessible to an online RSTS/E system only after the monitor is successfully SYSGENed and installed with the INSTALL suboption of the INIT.SYS program, and the units have been successfully initialized with the DSKINT suboption of INIT.SYS.

---

## **3.5.2 RT-11 Operating Systems (V5.1 and above)**

---

The RT-11 Operating System supports up to four MSCP controllers with up to 256 devices (total) on the four controllers. The following paragraphs discuss the LSI-11 bus and vector addresses for MSCP controllers under RT-11 in host systems with only one MSCP controller and in those with more than one controller. Disk partitioning, a unique feature of RT-11 that is applicable regardless of the number of controllers, is also discussed.

---

### **3.5.2.1 Installing a Single MSCP Controller**

---

If your host system includes only one MSCP controller, install it with a LSI-11 bus address of 772150<sub>8</sub>. RT-11 will find and install the handler (driver) for that controller. In single MSCP controller configurations, it is not necessary to run SYSGEN. You may use one of the pregenerated monitors that are provided with the RT-11 Distribution. Emulex recommends that you modify the system startup command file, STARTx.COM, to properly partition the disk drives. See subsection 3.5.2.3.

---

### **3.5.2.2 Installing Multiple MSCP Controllers**

---

If your host system includes more than one MSCP controller, you may either modify the MSCP handler as described in the *RT-11 Software Support Manual* or perform a SYSGEN. The following procedure describes the SYSGEN technique (user input is in bold print):

1. Initiate SYSGEN:

**IND SYSGEN**<return>

Answer the next group of questions appropriately.

2. Indicate that you want the system to use the startup command file when booting:

```
Do you want the startup indirect
file (Y)? Y<return>
```

The startup command file is required to allow additional MSCP controller LSI-11 bus addresses to be specified and to partition the disks consistently when the system is bootstrapped. Answer the next set of questions appropriately.

3. Indicate that you want MSCP support when the Disk Options question appears:

```
Enter the device name you want support for
[dd]: DU<return>
```

4. Indicate the number of MSCP controllers on your system in response to this question:

```
How many ports are to be
supported (1)? 2<return>
```

RT-11 refers to individual MSCP controllers or controllers as ports. Each port has its own LSI-11 bus and vector addresses.

5. Specify support for all other devices in your host system configuration as well. Indicate that there are no more devices by entering a period:

```
Enter the device name you want support for
[dd]: .<return>
```

6. You must specify the addresses of all MSCP controllers (ports) using the SET CSR keyboard command. To ensure that this is done consistently and automatically on power-up, you must add the commands to the system start-up command file, STARTx.COM. The x stands for the monitor that is being used, where x is S, F, or X for single-job, foreground/ background, or extended memory, respectively. Edit the command file to include the following statements:

```
SET DU CSR=772150           (Default)
SET DU CSR2=7603348       (Floating)
SET DU VECTOR=154         (Default)
SET DU VEC2=300
```

The LSI-11 bus for the second device can be any unused address in the I/O page which is supported by DM01 address switch settings; the vector address can be any unused address in the vector page. Default statements are not required.

7. Complete SYSGEN according to the DEC documentation.

---

### **3.5.2.3 Disk Partitioning**

---

RT-11 is unable to handle drives with a capacity of more than 65,535 blocks (33.5M bytes). To allow drives with larger capacities to be used, RT-11 allows individual physical drives to be partitioned into multiple logical drives. This is done by assigning as many logical drive names (DU0, DU1, etc.) to a physical drive as that drive can support. The statements that make that assignment should be placed in the system start-up command file. This ensures that the drives are automatically partitioned every time the system is bootstrapped and that the partitions are always the same. Use the following procedure to determine the total number of logical drives to be assigned to each physical drive.

1. Determine the drive configuration(s) that you intend to use. You need to know the LUN of each logical drive and the data storage capacity (in logical blocks) of each logical unit. If the DM01 is at an alternate LSI-11 bus address (not 772150<sub>8</sub>), then you must specify an MSCP device number by using switches SW1-2 through SW1-4.
2. Divide the capacity for each MSCP Unit by 65,535. If the result is a number greater than 1, then that MSCP Unit should be partitioned into multiple logical units. (The last partition on a disk may be smaller than 65,535 blocks.) Round the result up to the nearest whole number. That whole number (up to eight) equals the number of logical disks into which that MSCP unit should be partitioned.
3. You must then include a series of statements in the system startup command file, STARTx.COM, that assigns logical names to each partition. Each statement has the following format:

**SET DUn UNIT=y PART=x PORT=z**

where **n** is the logical device name, **y** is the physical MSCP unit number, **x** is the partition number, and **z** is the controller number (specify the controller number when two or more controllers are present; do not specify the port when only one controller is present). If you partition any drives, you must do this for each partition on each drive, including drives that can hold only one partition.

*Example:* You have selected a hard drive that has a capacity of 219,283 blocks (unit 0) and a floppy drive that has a capacity of 800 blocks (unit 1).

$$\frac{219,283}{65,535} = 3.35 \text{ (4 logical units)}$$

$$\frac{800}{65,535} = 0.012 \text{ (1 logical unit)}$$

Dividing the unit capacities by 65,535 and rounding the result up to the nearest whole number gives the number of logical units into which each should be partitioned.

You assign logical names to the partitions, beginning with DU0. For the previous example, the assignments are made as follows:

```
SET DU0 UNIT=0 PART=0
SET DU1 UNIT=0 PART=1
SET DU2 UNIT=0 PART=2
SET DU3 UNIT=0 PART=3
SET DU4 UNIT=1 PART=0
```

Because this configuration has only one controller, the port is not defined. If you had another controller in this configuration, the controllers would be defined as Port 0 and Port 1.

Modify the system startup command file to include the disk partitioning statements.

---

### 3.5.3 RSX-11M Operating Systems (V4.0 and above)

---

RSX-11M SYSGEN is an interrogative program that allows a complete, running RSX-11M system to be configured for a particular hardware environment. SYSGEN is well documented in the *RSX-11M System Generation and Installation Guide*, and you are expected to rely primarily on that manual. This explanation is provided only to remove some ambiguities that the installation of the DM01 may present.

SYSGEN supports autoconfigure, and MSCP controllers are detected by autoconfigure. However, autoconfigure detects only the MSCP controller that is located at the standard LSI-11 bus address. Additional MSCP controllers at alternate addresses must be attached to the operating system manually.

**NOTE**

If the DM01 controls the system disk, you must select 22-bit addressing (SW2-6 ON) even if your system has only 256K bytes of memory.

---

**3.5.3.1 Installing a Single MSCP Controller**

---

If you have only one DM01, install it at the standard address (772150<sub>8</sub>) and use autoconfigure to connect your peripherals. The procedure given in the *RSX-11M System Generation and Configuration Guide* is adequate for this purpose.

---

**3.5.3.2 Installing Multiple MSCP Controllers**

---

If you have two MSCP controllers, say a DEC MSCP controller and a DM01, you must perform a complete manual initialization. We recommend that the DEC MSCP controller be installed at the standard LSI-11 bus address. Locating the DM01 at the alternate LSI-11 bus address does not prevent its being used as the system device. Both MSCP controllers are connected to the operating system by using the following procedure.

1. Invoke SYSGEN.

```
> SET /UIC=[200,200]<return>
> @SYSGEN<return>
```

2. To indicate that you want to use autoconfigure, answer Y (yes) to the following question:

```
* Autoconfigure the host system hardware?
[Y/N]: Y<return>
```

3. To indicate that you do not want to override autoconfigure results, answer N (no) to this question:

```
* Do you want to override Autoconfigure results?
[Y/N]: N<return>
```

Answer the rest of the questions in the SETUP section appropriately, and continue to the next section, TARGET CONFIGURATION. In TARGET CONFIGURATION, the defaults presented for the first group of questions should be accurate for your system because autoconfigure was requested.



4. In response to the question regarding devices, indicate that you have two MSCP-TYPE controllers:

```
*  Devices: DU=2<return>
   Devices: .<return>
```

This entry supersedes the value of 1 that autoconfigure has determined. Typing a period (.) terminates device input.

Continue through the next four sections, HOST CONFIGURATION, EXECUTIVE OPTIONS, TERMINAL DRIVER OPTIONS, and SYSTEM OPTIONS, answering questions appropriately.

5. When you reach the PERIPHERAL OPTIONS section, SYSGEN asks you questions that pertain only to the MSCP devices on your system. (Unless you indicated that you wished to override other autoconfigure results when you responded to the Devices question, SYSGEN asks questions on those devices.)

The first question requests information about the controller's interrupt vector address, LSI-11 bus address, the number of DU-TYPE disk drives (there is no default value for this parameter), the number of command rings, and the number of response rings. The question is asked twice, once for controller 0 and once for controller 1, because we have specified two DU-TYPE controllers. The dialog uses the abbreviation *contr* to indicate controller.

```
*  DU contr 0 [D:154,172150,,4,4]
   154,172150,3,4,4<return>
```

The standard vector address for MSCP controllers is  $154_8$ . The vector for a second unit should be allocated from floating vector address space. Any unused vector between  $300_8$  and  $774_8$  can be allocated. See Appendix A for a description of DEC's algorithm for assigning floating vectors.

The standard LSI-11 bus address for MSCP controllers is  $772150_8$ . Emulex recommends that the second unit be located in floating LSI-11 bus address space. See Appendix A for a description of the DEC algorithm for assigning floating addresses.

The number of DU-TYPE disk drives depends on the configuration that you have selected for the DM01, or on the number of drives that are attached to a DEC MSCP controller.

When you select a configuration for the DM01, you are taking into account the number of physical disk drives that you are attaching to the DM01's peripheral interfaces. When you select a configuration, you are also specifying a logical arrangement for the DM01 MSCP subsystem. Some configurations split one physical drive into two logical drives to make file management easier. You determine the configuration of each drive when you program the DM01's NOVDRAM; see subsections 4.8, 4.9, and 4.10.

The following types of disk drives can be attached to DEC MSCP controllers:

- RX50
- RD51
- RD52
- RD53
- RC25
- RA60
- RA80
- RA81

The RX50 drive contains two 5.25-inch floppy diskettes; count an RX50 as two drives. The RC25 has both fixed and removable hard media; count an RC25 as two drives.

RSX-11M supports up to eight command and eight response rings; the number of command and response rings that you specify depends on your application. Four command and four response rings are reasonable and adequate for most applications.

6. SYSGEN then asks you to specify the type of disk drive(s) on each controller:

```
* DU contr 0 unit 0. is an RA60/80/81/RC25/RD51/RX50
[D:RA81] <return>
```

For the DEC MSCP controller, indicate the appropriate peripherals.

For the DM01, indicate that you have one RA81 for each logical disk drive; one RX50 for each floppy drive.

RSX-11M does not tolerate gaps in sequence; the unit numbers must be contiguous. In addition, the unit numbers specified for each controller must be the same as those reported by the controller during initialization.

7. Complete the SYSGEN procedure according to DEC documentation.

---

### 3.5.4 RSX-11M-PLUS Operating Systems (V2.1 and above)

---

RSX-11M-PLUS SYSGEN is an interrogative program that allows a complete, running RSX-11M-PLUS system to be configured for a particular hardware environment. Sysgen is well documented in the *RSX-11M-PLUS System Generation and Installation Guide*, and you are expected to rely primarily on that manual. This explanation is provided only to remove some ambiguities that the installation of the DM01 may involve.

SYSGEN supports autoconfigure, and MSCP controllers are detected by autoconfigure. However, autoconfigure detects only the MSCP controller that is located at the standard LSI-11 bus address. Additional MSCP controllers at alternate addresses must be attached to the operating system manually.

---

#### 3.5.4.1 Installing a Single MSCP Controller

---

If you have only one DM01, install it at the standard address (772150<sub>8</sub>) and use autoconfigure to connect your peripherals. The procedure given in the *RSX-11M-PLUS System Generation and Configuration Guide* is adequate for this purpose.

---

#### 3.5.4.2 Installing Multiple MSCP Controllers

---

If your initial system configuration includes two MSCP controllers, connect the alternate MSCP controller to the operating system during the initial SYSGEN. We recommend that you use autoconfigure to connect the first controller at the standard address (772150<sub>8</sub>). We recommend that the DEC MSCP controller be installed at the standard LSI-11 bus address; locating the DM01 at the alternate LSI-11 bus address does not prevent its being used as the system device.

If you are adding the second MSCP controller to the system configuration, use the Add a Device option of SYSGEN or a complete SYSGEN. The following procedure describes the Add a Device process (user input is in **boldface** print):

1. Invoke SYSGEN.
  - > **SET /UIC=[200,200]<return>**
  - > **@SYSGEN<return>**
  
2. To indicate that you want to do a subset of the SYSGEN procedure, answer **N** (no) to the following questions:
  - \* Do you want to do a complete SYSGEN?  
[Y/N D:Y]: **N<return>**
  
  - \* Do you want to continue a previous SYSGEN  
from some point? [Y/N D:Y]: **N<return>**

3. To indicate that you want to execute a specific module of the SYSGEN procedure, answer Y (yes) to this question:

\* Do you want to do any individual sections of SYSGEN? [Y/N D:Y]: Y<return>

4. Select the Add a Device section of SYSGEN:

\* Which sections would you like to do? [S R:0.15.]: H<return>

Type the letter H to select the Add a Device section. SYSGEN now asks you all of the questions in the Choosing Peripheral Configuration section.

The questions that SYSGEN asks pertain to the type and number of controllers that are installed on your system. There is one question for each type of controller that RSX-11M-PLUS can support. Answer 0 (zero) for all types of controllers until you are prompted for the number of UDA-TYPE devices.

5. When you are asked to specify the number of MSCP-type devices, answer appropriately:

\* How many MSCP disk controllers do you have? [D R:0.63. D:0.] 2<return>

6. Give the total number of MSCP disk drives (on all controllers) installed on the system.

\* How many MSCP disk drives do you have? [D R:0.n. D:1.] 5<return>

The answer to this question depends on the configuration that you have selected for the DM01 and on the number of drives that are attached to any DEC MSCP controllers.

When you select a configuration for the DM01, you are taking into account the number of physical disk drives that you are attaching to the DM01's interfaces. When you select a configuration, you are also specifying a logical arrangement for the DM01 MSCP subsystem. Some configurations split one physical drive into two logical drives to make file management easier. You determine the configuration of each disk drive when you program the DM01's NOVRAM (see subsections 4.8, 4.9, and 4.10).

The following types of disk drives can be attached to DEC MSCP controllers:

- RX50
- RD51
- RD52
- RD53
- RC25
- RA60
- RA80
- RA81

The RX50 drive contains two 5.25-inch floppy diskettes; count an RX50 as two drives. The RC25 drive has both fixed and removable hard media; count an RC25 as two drives.

7. SYSGEN then asks you to specify controllers per disk drives.

\* To which DU controller is DU0: connected?  
[S R:11]: A<return>

This question is asked as many times as the number of MSCP drives that you have indicated are on the system. RSX-11M-PLUS does not tolerate gaps in sequence; the MSCP unit numbers must be contiguous. In addition, the unit numbers specified for each controller must be the same as those reported by the controller during initialization. Use A for the primary controller and B for the alternate controller.

8. Enter the vector address for each MSCP controller:

\* Enter the vector address of DUA  
[O R:60774 D:154]

The standard vector address for MSCP controllers is  $154_8$ . The vector for a second unit should be allocated from floating vector address space. Any unused vector between  $300_8$  and  $774_8$  can be allocated. See Appendix A for a description of DEC's algorithm for assigning floating vectors.

9. Enter the CSR address for each MSCP controller:

\* What is its CSR address?  
[O R:160000177700 D:172150]

The standard CSR address for MSCP controllers is  $772150_8$ . Emulex recommends that the second unit be located in floating CSR address space. See Appendix A for a description of the DEC algorithm for assigning floating addresses.

10. Specify the number of command rings for each MSCP controller:

\* Enter the number of command rings for DUA  
[D R:1.8. D:4.] 4<return>

RSX-11M-PLUS supports up to eight command rings. The value you specify depends on your application. Four command rings are reasonable and adequate for most applications.

11. Specify the number of response rings for each MSCP controller:

\* Enter the number of response rings for DUA  
[D R:1.8. D:4.] 4<return>

RSX-11M-PLUS supports up to eight response rings. The value you specify depends on your application. Four response rings are reasonable and adequate for most applications.

---

### 3.5.5 MicroVMS Operating Systems

---

MicroVAX/MicroVMS supports MSCP controllers at the standard address, 772150<sub>g</sub>, and in floating address space. MicroVMS has a software utility called SYSGEN which can be used to determine the LSI-11 bus address and interrupt vector address for any I/O devices to be installed on the computer's LSI-11 bus. A running MicroVAX/MicroVMS computer system is required in order to use this utility.

If you do not have access to a running system, you must determine the LSI-11 bus addresses and vector addresses manually (although autoconfigure can still be used to connect the devices to the computer automatically on powerup). See Appendix A for a description of the algorithm used by SYSGEN to determine LSI-11 bus addresses.

The following procedure tells how to use MicroVMS SYSGEN to determine LSI-11 bus addresses and interrupt vectors.

1. Log in to the system manager's account. Run the SYSGEN utility:

```
$ RUN SYSSYSTEM:SYSGEN<return>
SYSGEN>
```

The SYSGEN > prompt indicates that the utility is ready to accept commands.

2. Obtain a list of devices already installed on the MicroVAX LSI-11 bus by typing:

```
SYSGEN> SHOW/CONFIGURATION<return>
```

```
Name: PUA  Units: 1  Nexus: 0  CSR: 772150  Vector1: 154  Vector2: 000
Name: TTA  Units: 1  Nexus: 0  CSR: 760100*  Vector1: 300*  Vector2: 304*
Name: TXA  Units: 1  Nexus: 0  CSR: 760500*  Vector1: 310*  Vector2: 000
```

\*Floating address or vector

Note: All addresses and vectors are expressed in octal notation.

Figure 3-3. Sample SHOW CONFIGURATION

SYSGEN lists by logical name the devices already installed on the LSI-11 bus. Make a note of these other devices with floating addresses (greater than 760000<sub>8</sub>) or floating vectors (greater than 300<sub>8</sub>) that you plan to reinstall with your DM01.

3. To determine the LSI-11 bus addresses and vectors that autoconfigure expects for a particular device type, execute the CONFIGURE command:

```
SYSGEN> CONFIGURE<return>
DEVICE>
```

Specify the LSI-11 bus devices to be installed by typing their LSI-11 bus names at the DEVICE prompt (the device name for MSCP controllers under MicroVMS is UDA).

```
DEVICE> UDA,2<return>
DEVICE> DHV11<return>
DEVICE> DZ11<return>
```

A comma separates the device name from the number of devices of that type to be installed. The number of devices is specified in decimal.

In addition to the DM01, you need only specify devices that have floating addresses or vectors. Devices with fixed addresses or vectors do not affect the address or vector assignments of devices with floating addresses and vectors.

4. Indicate that all devices have been entered by pressing the <ctrl> and Z keys simultaneously:

```
DEVICE> ^Z
```

SYSGEN lists the addresses and vectors of the devices entered in the format shown in Figure 3-4.

```
SYSGEN> CONFIGURE
DEVICE> DZ11
DEVICE> DHV11
DEVICE> UDA,2
DEVICE> ^Z
Device: UDA      Name: PUA CSR: 772150  Vector: 154  Support: yes
Device: DZ11     Name: TTA CSR: 760100* Vector: 300*  Support: yes
Device: UDA      Name: PUB CSR: 760354* Vector: 310  Support: yes
Device: DHV11    Name: TXA CSR: 760500* Vector: 320  Support: yes
```

\*Floating address or vector

Note: All addresses and vectors are expressed in octal notation.

Figure 3-4. CONFIGURE Command Listing

5. Note the CSR addresses listed for the LSI-11 bus devices in floating address space. Program the listed addresses into non-Emulex devices as instructed by the manufacturer's documentation. For the DM01, program the address given for the DM01 (lowest numerical address) into the board as described in subsection 4.3.1.
6. Complete SYSGEN according to the DEC documentation.

If you want to select a nonstandard address for the DM01, that is one that differs from the address selected by the CONFIGURE command, you must enter CONNECT statements in the SYCONFIG.COM file that is in the system manager's account, SYS\$MANAGER. Use the syntax of the CONNECT statements as described in the DEC documentation on MicroVMS SYSGEN.

#### NOTE

Do not alter the STARTUP.COM or UVSTARTUP.COM command files in the main system account, SYS\$SYSTEM.

---

### 3.5.6 Ultrix-11 Operating Systems (V3.0 and above)

---

The Ultrix-11 Version 3.0 system supports up to a total of four MSCP disk controllers, but only one of each type of controller. Therefore, to add support for two MSCP controllers, the system generation procedure must be told that there is, for example, one UDA50 controller and one RQDX1 controller. The choices are:

<u>Controller name</u>	<u>device name</u>	<u>disk name</u>
UDA50	ra	ra??
KLESI	rc	rc??
RQDX1, RQDX2, or RQDX3	rq	rd??
RUX1	rx	rx??



**NOTE**

A bug exists in version 3.0 that prevents actually using more than three controllers. When an RQDX1, RQDX2, or RQDX3 is specified, the sysgen program will not allow specifying an RUX1 controller, and vice versa.

---

**3.5.6.1 Sysgen**

---

To add a device to an Ultrix-11 kernel, the sysgen program must be run to create and make a new kernel. Creating a kernel involves the creation of a configuration file and then "making" the kernel from this configuration file.

A dialogue mode is used to enter various system parameters. The question:

Disk controller type:

```
<rh11 rh70 rp11 rk611 rk711 rl11 rx211 rk11
uda50 kda50 rqdx1 rqdx2 rqdx3 klesi rux1> ?
```

asks for the specification of a disk controller. You must choose a different controller type for each MSCP controller on your system, even if they are all DM01s.

**NOTE**

The order in which you enter each controller is very important. The order becomes the controller number. The same order must also be used when creating the special files (see below).

For each MSCP controller specified, one of the following statements will be typed:

First MSCP controller type:  
Second MSCP controller type:  
Third MSCP controller type:

and, depending on the controller name specified previously, the next question will differ. See the appropriate correlation below:

<u>Disk Controller Type</u>	<u>Next Sysgen Question</u>
uda50 or kda50	Drive 0 type < ra60 ra80 ra81 > ?
klesi	Drive 0 type < rc25 > ?
rqdx1/2/3 or rux1	Drive 0 type < rx50 rd51 rd52 rd53 > ?

Note that it doesn't matter which drive type you choose. Just enter one of the supplied names for each drive you have connected to each controller.

The next two questions refer to the controller's CSR and vector addresses:

```
CSR address <172150> ?  
Vector address <154> ?
```

The defaults for the CSR and Vector address will always be 172150 and 154, respectively. Be sure to enter the correct CSR value. Since the MSCP controller accepts a software-defined vector, an unused vector from the floating address space should be used for all nonstandard address controllers. Emulex suggests that you use a decrementing (by 4) vector address starting at 700 (octal). This will prevent you from using a vector address that is already in use.

---

### **3.5.6.2 Special Files**

---

The Ultrix operating system communicates with devices on the system by the use of special files. These files contain pointers into a system table that lists the entry points for a corresponding driver for that device. There must be a special file for each device (and each partition for disks) on the system in order for Ultrix to communicate with that device. Some devices will have two special files associated with a device: one for use with character mode, and the other for block mode. These special files exist in the account `"/dev"`.

The special files for Ultrix-11 are created with the `'msf'` program (make special file). If no options are supplied, this program enters a dialogue mode:

```
# /etc/msf
```

The `"msf"` program will issue the prompt:

```
Command <create exit help remove table>:
```

Use the `"c"` command to create the special files.

```
Device name (? for help) <rp06,dz11,lp11,etc>:
```

The `"msf"` program does not understand the notations for different controller types. Instead, it uses the device name and controller number in order to create the special files. For example, the special files for `ra60`, `ra80`, and `ra81` would be `"ra"`, the special file for an `rc25` would be `"rc"`, and the special files for an `rd51/2/3` would be `"rd"`. Therefore, you must enter a unique device name for each controller. It is suggested that you use the same device names used previously with the `sysgen` program.

The next two questions are:

MSCP controller number <0 1 2 3>:

Unit number <0 -> 7 or all>:

The MSCP controller number assigned to each controller is determined by the order in which you entered the devices to the sysgen program; that is, the number for the first controller is 0.

The unit number for each drive (as it is identified by SW1-2 through SW1-4) must match the drive's specification in the configuration file. In addition, the drive to be booted from must be 0, regardless of whether the controller is at the standard or an alternate address.

For ra, rc, and rd type devices, the next question will be asked:

Assume standard disk partitions (? for help) <y or n> ?

If you answer "no", the next question will be asked:

Create partitions <0 -> 7 or all>?

You should always answer "all".

---

### 3.5.6.3 Newfs

---

The "newfs" program is used to create file systems on specified partitions. The newfs program requires no arguments and immediately enters a dialogue mode. See the *Ultrix-11 System Manager's Guide* for more information on newfs.

---

### 3.5.6.4 Volcopy

---

Once a device is configured into your current kernel, you can copy an existing file system onto a new partition with the 'volcopy' program. The new partition will be created with the identical size parameters of the original file system. See the *Ultrix-11 System Manager's Guide* for more information on volcopy.

---

### 3.5.6.5 Copying a Bootstrap

---

A new bootstrap can be copied onto a new system disk with the "dd" program. The command:

```
# dd if=/mdec/rauboot of=/dev/ra00
```

will copy the bootstrap file onto block zero of ra0.

**NOTE**

V7M-11 V1.0 and Ultrix-11 V2.0 did not support self-sizing disks and are, therefore, unusable with the Emulex MSCP controllers.

---

**3.5.7 Ultrix-32 Operating Systems**

---

The Emulex MSCP class disk subsystems emulate the DEC DSA UDA-50/KDA-50/RA81 (MSCP) disk subsystem. They report that they are of controller type "DU" and of device type "RA81". However, when asked for the number of logical blocks, they do not return a size value that matches that of a "real" DEC RA81.

---

**3.5.7.1 The Kernel**

---

Support for MSCP controllers must be included in a monitor when rebuilding the kernel. The configuration file is edited to reflect the number of controllers and the number of drives connected to each controller. The Ultrix-32 system supports two MSCP disk controllers.

Ultrix-32 does not require that the units be in sequential order. However, the MSCP device number for the drive to be booted from **MUST** be 0 regardless of the controller's LSI-11 bus address. Further, be certain that the MSCP device number of each drive (as it is identified by SW1-2 through SW1-4) matches the drive's specification in the configuration file.

The following example shows two controllers, the first with two drives, the second with one:

```
controller  uda0    at uba0 csr 0172150      vector udintr
disk        ra0     at uda0 drive 0
disk        ra1     at uda0 drive 1
controller  uda1    at uba0 csr 0160334      vector udintr
disk        ra2     at uda1 drive 2
```

In this example, the first unit on the second controller must be MSCP device number two regardless of the units on the first controller.

---

**3.5.7.2 Special Files**

---

The Ultrix operating system communicates with devices on the system by the use of special files. These files contain pointers into a system table that lists the entry points for a corresponding driver for that device. There must be a special file for each device (and each partition for disks) on the system in order for Ultrix to communicate with that device. Some devices will have two special files associated with a device: one for use with character mode, and the other for block mode. These special files exist in the account "/dev".

There is a shell script, called "MAKEDEV" (uppercase important), on the Ultrix-32 system to help build these special files. The format of this command is:

```
% /dev/MAKEDEV device ...
```

This script passes your input to the program "mknod" to create the special files. You should use this command file to create the special files for each disk you wish to connect to the system. An example for two disks is:

```
% /dev/MAKEDEV ra4 ra5
```

This example assumes that you have already added the device into the configuration file, and you chose the logical names ra4 and ra5 for your disks.

---

### 3.5.7.3 Autoconfigure

---

At boot time, Ultrix-32 attempts to auto-configure the devices included in the booted monitors configuration file. If the device was not included in the configuration files, it will not be configured into the running system. If the device is not present, Ultrix will skip it.

When Ultrix-32 finds a device at autoconfigure time it prints a message as follows:

```
rqd0 at csr 172150 vec 774, ipl 17
ra0 at rqd0 slave 0
ra1 at rqd0 slave 0
rqd1 at csr 160334 vec 770, ipl 17
ra2 at rqd1 slave 0
```

The CSR address were set in the configuration file. The vectors are assigned sequentially in reverse order by the operating system. If the CSR or unit numbers don't match the configuration file, the device will be skipped (and no message will be printed).

---

### 3.5.7.4 Disk Partitions

---

Ultrix allows a user to logically subdivide a disk into sections called "partitions". Disk partitions were created because the first Unix operating systems could access only a limited amount of space on large disks. Disk partitioning lets several Unix file systems reside on the same disk, one file system per partition. This allows the operating system to utilize the entire disk.

Each disk has a partition table that defines the starting location and size (both in blocks) of each partition on that disk. When a disk is opened by the operating system (for the very first time), it writes the partition size table into the super block of partition "a" (the first partition) on the disk.

---

### 3.5.7.5 Disk Partition Modifications

---

Modifications to a disk's partition table is done with the "chpt" command each time a disk is initialized or reinitialized. The "chpt" command allows a system manager to alter a particular partition's location and size characteristic.

The operating system initializes the disk's partition table with that of a real DEC RA81's size table (found in the disk driver) on its first opening. The system manager should then edit these sizes (with "chpt" command) to match the system needs.

---

### 3.5.7.6 Default Partition Modifications

---

It is also possible to modify the default RA81 partition size table, which is stored in the device driver; this would eliminate the need for editing the partition table each time the disk is initialized.

When DEC reorganized the Berkeley 4.2 Unix system to create Ultrix-32, they set it up to allow the distribution of the operating system in a binary format. This allowed them to distribute a minimum amount of source code to binary license holders. They separated each of the drivers and system kernel modules into two sections: a code portion and a data portion. The code portion does not require recompilation depending on the selected options at SYSGEN time; this is supplied in object format (xx.o). The data portion requires selection parameters based on sysgen answers; this is supplied in source code format (xx\_data.c). Making changes to this table will alter the default partition size characteristics for new disks. An example of the changes to the uda driver is included here.

Example:

```
/usr/sys/data/uda_data.c:

), ra81 sizes[8] ={
    15884, 0,          /* A=blk 0 thru 15883 */
    66880, 15884,     /* B=blk 15884 thru 82763 */
    -1, 0,           /* C=blk 0 thru end */
    0, 0,            /* D= not used */
    0, 0,            /* E= not used */
    0, 0,            /* F= not used */
    -1, 82764,       /* G=blk 82764 thru end */
    0, 0,            /* H= not used */
};
```

The -1 above indicates the end of the disk.

---

### 3.5.7.7 Newfs

---

The newfs program speeds up the creating of a file system on a partition. It looks up information, in the file /etc/disktab, on the disk specified by the system manager and creates the file system according to those default values. An example of the changes to the /etc/disktab file have been included here.

Example:

/etc/disktab:

```
qd21|DM01|Emulex DM01 Fujitsu Eagle M2351A Winchester:\
      :ty=winchester:ns#47:nt#20:nc#840:\
      :pa#15884:ba#4096:fa#512:\
      :pb#66880:bb#4096:fb#512:\
      :pc#789600:bc#4096:fc#1024:\
      :pg#706836:bg#4096:fg#1024:
```

---

### 3.5.7.8 Suggestions/Warnings

---

There is a maximum of eight partitions per disk. The partitions form logical boundaries on the disk, separating each file system from all others. These logical divisions are useful for disk management because you can put similar types of users, files, directories or projects all on the same file system. Because a file system can never exceed its partition in size, you can use partitions to regulate disk use.

There are certain areas of the disk which, by default, are reserved for the operating system. By mounting the swap space, for example, on its own partition, important data can not be overwritten when data from memory is swapped to the disk. The Ultrix-32 systems use partition "b" for the swap file. If you plan to use your own partition values, be sure to allocate an area on your system disk for a swap file.

For more information on disk partitioning and modifications to the partition sizes, see Appendix C, "Disk Partitioning", in the *Ultrix-32 System Manager's Guide*.

The program "diskpart" is used to create entries for the disk driver or for the "disktab" file. It creates a template based on the default rules used at Berkeley. The following is a table defining the Berkeley defaults:

<u>Partition</u>	<u>20-60 MB</u>	<u>61-205MB</u>	<u>206-355 MB</u>	<u>356+ MB</u>
a	15884	15884	15884	15884
b	10032	33440	33440	66880
c *	all	all	all	all
d	15884	15884	15884	15884
e	unused	55936	55936	307200
f *	unused	end	end	end
g *	end	end	end	end

\* The 'c' partition is, by convention, used to access the entire disk. In normal operation, either the 'g' partition is used, or the 'd', 'e', and 'f' partitions are used. The 'f' and 'g' partitions are variable sized, occupying whatever space remains after allocation of the fixed sized partitions.

#### NOTE

Ultrix-32 V1.0 did not support self-sizing disks and is, therefore, unusable with the Emulex MSCP controllers. The "diskpart" program was not included on the Ultrix-32 V1.1 distribution kit.



---

## **4.1 Overview**

---

The procedure for installing the DM01 Disk Controller is described in this section. The subsection titles are listed below to serve as an outline of the procedure.

Subsection	Title
4.2	Inspection
4.3	Disk Controller Setup
4.4	Physical Installation
4.5	ST-506 Drive Preparation
4.6	SA450 Drive Preparation
4.7	Cabling
4.8	NOVRAM Loading, Disk Formatting, and Testing
4.9	F.R.D. Options
4.10	Drive Configuration Parameters
4.11	Operation

If you are unfamiliar with the subsystem installation procedure, Emulex recommends reading this Installation Section before beginning.

---

### **4.1.1 Subsystem Configurations**

---

This section is limited to switch setting data and physical installation instructions. No attempt is made to describe the many subsystem configurations that are possible. **If you are not familiar with the possible configurations, we strongly recommend reading Section 3, PLANNING THE INSTALLATION, before attempting to install this subsystem.**

When you are installing the subsystem, you should make a record of the subsystem configuration and environment. Figure 4-1 is a Configuration Record Sheet that lists the information required and shows where the data can be found. This information will be of help to an Emulex service representative should your subsystem require service.

### DM01 CONFIGURATION REFERENCE SHEET

**GENERAL INFORMATION**

- Host computer type \_\_\_\_\_
- Host computer operating system \_\_\_\_\_  
Version \_\_\_\_\_
- Other MSCP Controllers; Type \_\_\_\_\_, LSI-11 bus address \_\_\_\_\_

**DRIVE CONFIGURATION PARAMETERS**

- Hard Disk Model (0) \_\_\_\_\_ Hard Disk Model (1) \_\_\_\_\_
- Hard Disk Model (0) \_\_\_\_\_ Hard Disk Model (1) \_\_\_\_\_
- NOVRAM Parameters:

**HARD DISK DRIVE 0**

1	Number Units	_____
2	Type Code	_____
3	Head Offset	_____
4	Sectors/Track	_____
5	Heads	_____
6	Cylinders	_____
7	Spare Sectors	_____
8	Alternate Cylinders	_____
9	Configuration Bits	_____
10	Split Code	_____
11	Removable Media	_____
12	Gap 0	_____
13	Gap 1	_____
14	Gap 2	_____
15	Cylinder Offset	_____
16	Spiral Offset	_____

**HARD DISK DRIVE 1**

1	Number Units	_____
2	Type Code	_____
3	Head Offset	_____
4	Sectors/Track	_____
5	Heads	_____
6	Cylinders	_____
7	Spare Sectors	_____
8	Alternate Cylinders	_____
9	Configuration Bits	_____
10	Split Code	_____
11	Removable Media	_____
12	Gap 0	_____
13	Gap 1	_____
14	Gap 2	_____
15	Cylinder Offset	_____
16	Spiral Offset	_____

**FLOPPY DISK DRIVE 0**

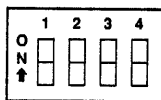
1	Number Units	_____
2	Type Code	_____
15	Step Code	_____

**FLOPPY DISK DRIVE 1**

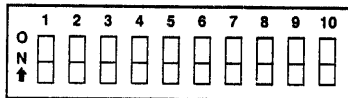
1	Number Units	_____
2	Type Code	_____
15	Step Code	_____

**DM01 CONFIGURATION**

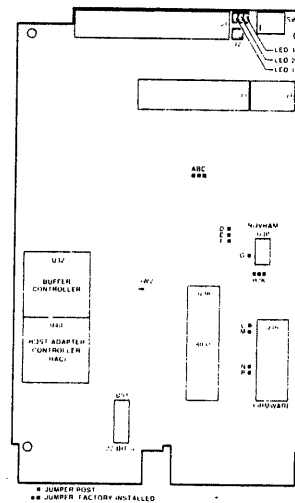
- Firmware revision number \_\_\_\_\_
- Top assembly number \_\_\_\_\_
- LSI-11 bus address \_\_\_\_\_
- 22-bit addressing IC (7438) installed (Y or N) \_\_\_\_\_
- Switch settings ( = OFF  = ON)
- Warranty expiration date \_\_\_\_\_
- Serial number \_\_\_\_\_
- Interrupt vector address \_\_\_\_\_



SW1



SW2



Use Pencil

Figure 4-1. DM01 Configuration Reference Sheet

DM0102-0665

### 4.1.2 DIP Switch Type

Switch-setting tables in this manual use the numeral one (1) to indicate the ON (closed) position and the numeral zero (0) to indicate the OFF (open) position.

The two DIP switch types used in this product are shown in Figure 4-2. Each is set to the code shown in the switch setting example.

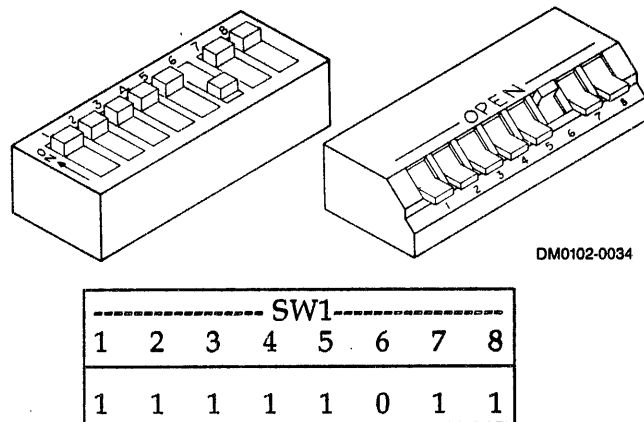


Figure 4-2. Switch Setting Example

### 4.1.3 Maintaining FCC Class A Compliance

Emulex has tested the DM01 Intelligent Disk Controller with DEC computers that comply with FCC Class A limits for radiated and conducted interference. When properly installed, the DM01 does not cause compliant computers to exceed Class A limits.

There are two possible configurations in which the DM01 and its associated ST-506 peripherals can be installed:

- With both the DM01 Disk Controller and the hard and floppy disk drives mounted in the same cabinet, and
- With the DM01 and the floppy disk drive mounted in the CPU cabinet and the hard disk drives mounted in a separate cabinet.

To limit radiated interference, DEC completely encloses the components of its computers that generate or could conduct radio-frequency interference (RFI) with a grounded metal shield (earth ground). During installation of the DM01, nothing must be done that would reduce this shield's effectiveness. That is, when the DM01 installation is complete, no gap in the shield that would allow RFI to escape can be allowed.

Conducted interference is generally prevented by installing a filter in the ac line between the computer and the ac outlet. Most power distribution panels that are of current manufacture contain suitable filters.

---

## **4.2 Inspection**

---

Emulex products are shipped in special containers designed to provide full protection under normal transit conditions. Immediately upon receipt, the shipping container should be inspected for evidence of possible damage incurred in transit. Any obvious damage to the container, or indications of actual or probable equipment damage, should be reported to the carrier company in accordance with instructions on the form included in the container.

Unpack the DM01 subsystem and, using the shipping invoice, verify that all equipment is present. Verify also that model or part numbers (P/N), revision levels, and serial numbers agree with those on the shipping invoice. These verifications are important to confirm warranty. If evidence of physical damage or identity mismatch is found, notify an Emulex representative immediately. If the equipment must be returned to Emulex, it should be shipped in the original container.

Visually inspect the DM01 Disk Controller after unpacking. Check for such items as bent or broken connector pins, damaged components, or any other evidence of physical damage.

Examine all socketed components carefully to ensure that they are properly seated.

---

## **4.3 Disk Controller Setup**

---

Several configuration setups must be made on the DM01 Disk Controller before inserting it into the chassis. These setups are made by option switches SW1 and SW2.

Figure 4-3 shows the locations of the configuration switches referenced in the following paragraphs.

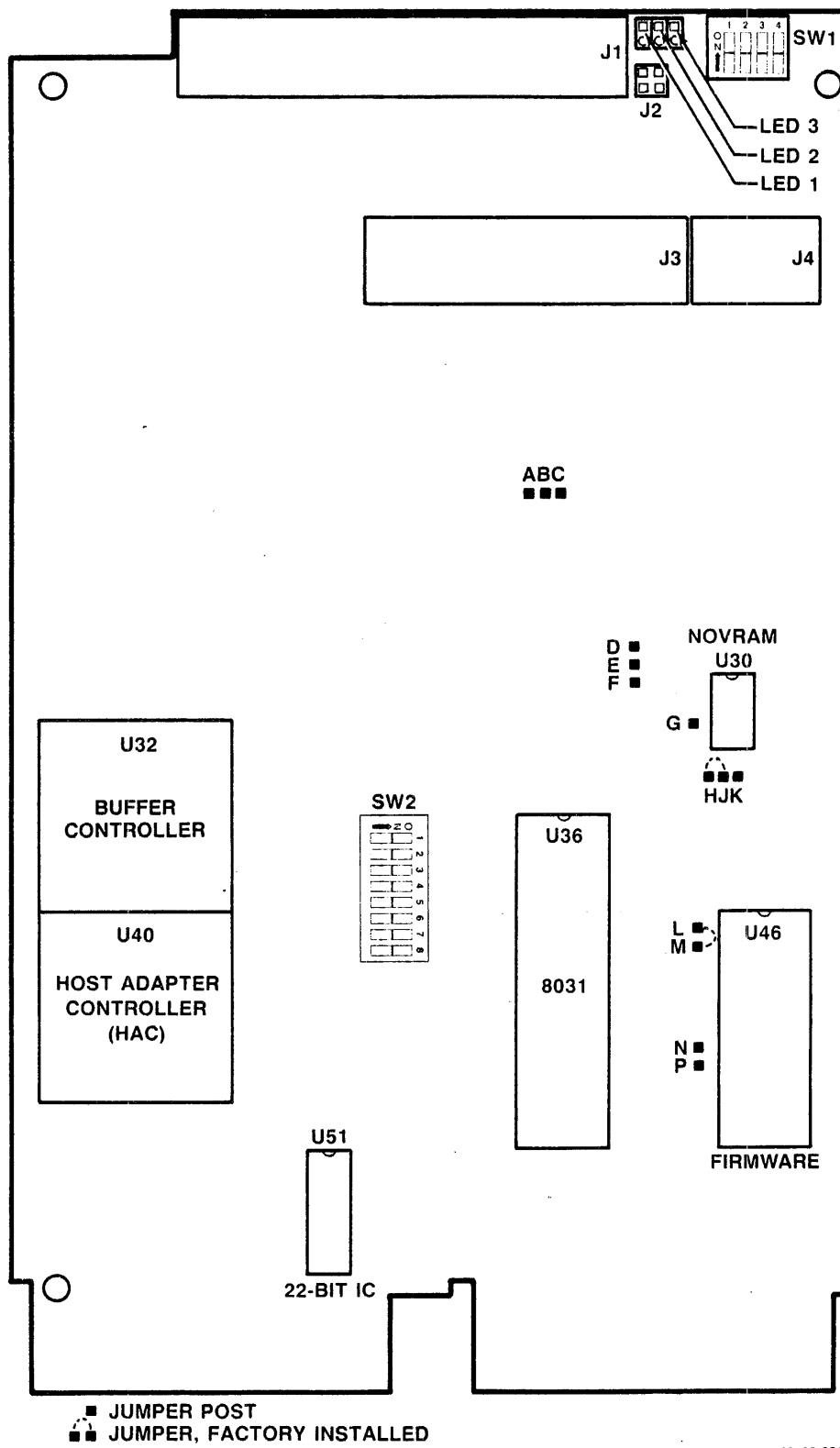


Figure 4-3. DM01 Disk Controller Assembly

**NOTE**

If you change a switch position on the DM01, or change the configuration values in the NOVRAM, you must also reset the DM01 so that the host operating system's initialization sequence reads the codes established by the switch settings and/or NOVRAM. To reset the DM01, either toggle switch SW1-1 (ON then OFF), or power-down and power-up the system. **If you toggle SW1-1, be sure the system is offline. Resetting the controller with the system running is likely to crash the system.**

Table 4-1 defines the function and factory configuration of all switches on the DM01 controller. The factory configuration switch settings are representative of most DM01 Disk Controller applications.

Table 4-1. DM01 Switch Definitions and Factory Configuration

SW	OFF(0)	ON(1)	FACT	Function	Section
SW1-1	Run	Reset/Halt	OFF(0)	Run vs.Reset/Halt	
SW1-2	-	-	OFF(0)	MSCP Device Number (LSB)	4.3.3.2
SW1-3	-	-	OFF(0)	MSCP Device Number	4.3.3.2
SW1-4	-	-	OFF(0)	MSCP Device Number (MSB)	4.3.3.2
SW2-1	Disable	Enable	OFF(0)	Loop on Self-Test Error	
SW2-2	Disable	Enable	OFF(0)	Automatic Bootstrap	4.3.3.1
SW2-3	-	-	OFF(0)	LSI-11 Bus Address	4.3.1
SW2-4	-	-	OFF(0)	LSI-11 Bus Address	4.3.1
SW2-5	-	-	OFF(0)	LSI-11 Bus Address	4.3.1
SW2-6	18-bit	22-bit	OFF(0)	22-Bit Addressing	4.3.3.3
SW2-7	4 usec	8 usec	OFF(0)	DMA Burst Delay	4.3.3.4
SW2-8	Enable	Disable	OFF(0)	Adaptive DMA Mode	4.3.3.5
ON(1) = Closed                      FACT = Factory Setting OFF(0) = Open					

Table 4-2 lists the function and factory configuration of all jumpers on the controller.

Table 4-2. DM01 Jumper Definitions and Factory Configuration

Jumper	OUT	IN	FACT	Comment
A-B-C	Not Used	Not Used	OUT	Must be OUT
D-E-F	Not Used	Not Used	OUT	Must be OUT
G	Not Used	Not Used	OUT	Must be OUT
H-J	Disable Clock	Enable Clock	IN	Must be IN
K	Normal Operation	Ground (Test)	OUT	Must be OUT
L-M	16K PROM Select	32K PROM Select	IN	Must be IN
N-P	Normal Operation	Factory Test	OUT	Must be OUT
FACT = Factory Setting				

### 4.3.1 Disk Controller Bus Address

Every LSI-11 bus I/O device has a block of several registers through which the system can command and monitor that device. The registers are addressed sequentially from a starting address assigned to that controller, in this case an MSCP-class disk controller.

The base address for the DM01's two LSI-11 bus registers is selected by DIP switches SW2-3 through SW2-5. See Table 4-3 for register address switch settings.

Table 4-3. Controller Address Switch Settings

Bus Address (in octal)	--- SW2 ---			Factory
	3	4	5	
772150	0	0	0	
772154	1	0	0	
760334	0	1	0	
760340	1	1	0	
760344	0	0	1	
760350	1	0	1	
760354	0	1	1	
760360	1	1	1	

---

### 4.3.2 Interrupt Vector Address

---

The interrupt vector address for the DM01 is programmed into the device by the operating system during the MSCP initialization sequence. See subsection 3.5 for a discussion of vector addresses.

---

### 4.3.3 Options

---

There are other DM01 options that can be implemented by the user. These features are selected by physically installing the option on the PCBA or by enabling the option using a switch.

---

#### 4.3.3.1 Automatic Bootstrapping

---

The automatic bootstrapping option causes the system to boot automatically from logical unit 0 through 5 on power-up when the DM01 is at the standard base address. To enable this option, set SW2-2 ON and set switches SW1-2 through SW1-4 as described in Table 4-4. This option should not be enabled with a MicroVAX or in a system that uses an 11/73B CPU module.

Switch	OFF	ON	Factory
SW2-2	Disable	Enable	OFF

The automatic bootstrapping process requires that the LSI-11 CPU module be configured for power-up mode 0. The following table lists the configuration settings for several popular LSI-11 CPUs.

CPU	Configuration Setting
11/73A	Install W3 and W7
11/23+	Remove J18-J19 and J18-J17
11/23	Remove W5 and W6
11/02	Remove W5 and W6

If the bootstrap device is not powered-up or safe (e.g., it failed its self-test), the autoboot routine in the DM01 halts the CPU after 1 minute. This causes the CPU to enter Console ODT. You can then examine the Status and Address (SA) register (base address plus 2) for an MSCP error code (Table 5-3) and bootstrap the system from another device.



### 4.3.3.2 MSCP Device Number

DM01 switches SW1-2 through SW1-4 specify MSCP device numbers. The functions of these switches are dependent on the options you select for your DM01:

- If the DM01 is installed at the standard LSI-11 bus address, these switches identify the MSCP device number of the drive from which to bootstrap. The DM01 automatic bootstrap option supports only MSCP units 0 through 5 at the standard address.
- If the DM01 is installed at an alternate LSI-11 bus address, these switches identify the MSCP device number of the first drive supported by that alternate DM01. The first drive supported by the DM01 at an alternate address may be drive 0 through 7.

### 4.3.3.2.1 Logical Unit to Boot From

If the DM01 automatic bootstrapping option is enabled (SW2-2 ON) and the DM01 is at the standard LSI-11 bus address (772150<sub>8</sub>), switches SW1-2 through SW1-4 define the MSCP device number of the drive from which the DM01 bootstraps. By using these switches, you may select one of four logical units to bootstrap from. Table 4-4 defines the MSCP device numbers selected by switches SW1-2 through SW1-4 if the DM01 is at a standard address.

Table 4-4. Bootstrap MSCP Device Number

Bootstrap MSCP Device Number	----- SW1 -----			Factory
	2 (LSB)	3	4 (MSB)	
0	0	0	0	
1	1	0	0	
2	0	1	0	
3	1	1	0	
4	0	0	1	
5	1	0	1	

### 4.3.3.2.2 First Logical Unit Number for an Alternate Address

If your DM01 is installed at an alternate address, switches SW1-2 through SW1-4 select the MSCP device number of the first drive supported by the DM01. MSCP device numbering schemes may vary by DEC operating system (see subsection 3.4.2.2). Table 4-5 defines the MSCP device numbers selected by switches SW1-2 through SW1-4 if the DM01 is at an alternate address.

*Example 4-1:*

*Your system operates under RSX-11M-PLUS and has two DM01 Disk Controllers. The first DM01 is at the standard base address for MSCP controllers (772150<sub>8</sub>) and supports three logical drives: Unit 0, Unit 1, and Unit 2. The second DM01 is at an alternate base address and supports two logical drives. RSX-11M-PLUS requires that the first drive on the alternate DM01 have an MSCP device number of 3 and that the second drive have an MSCP device number of 4. On the alternate DM01, set switches SW1-2 in the ON position, SW1-3 in the ON position, and SW1-4 in the OFF position to specify an MSCP device number of 3 for the first drive.*

*This example would also apply if the first MSCP controller were a DEC MSCP controller with three logical drives.*

Table 4-5. MSCP Device Number for the First Drive Supported by a DM01 at an Alternate Address

Starting MSCP Device Number	----- SW1 -----		
	2 (LSB)	3	4 (MSB)
0	0	0	0
1	1	0	0
2	0	1	0
3	1	1	0
4	0	0	1
5	1	0	1
6	0	1	1
7	1	1	1

---

**4.3.3.3 22-Bit Memory Addressing**

---

The 22-bit addressing capability is a standard option for the DM01 and is supplied with the DM01 as a kit, part number QD01113002. To enable 22-bit addressing, install the single 7438 IC in socket U51 on the DM01 PCBA and set SW2-6 ON (1). 22-bit addressing must be enabled with MicroVAX systems.

**CAUTION**

Some manufacturers of LSI-11 bus backplanes use the backplane lines now devoted to extended addressing for power distribution. Installing a DM01 with the extended addressing option in such a system will damage the option IC. Before installing the option, confirm that there is neither positive nor negative potential between lines BC1, BD1, BE1, BF1, and logic ground. A DM01 without the extended addressing option will not be damaged if power is present on those lines.

**4.3.3.4 DMA Burst Delay**

The DM01 firmware design includes a switch-selectable DMA burst delay to avoid data-late conditions. Switch SW2-7 selects either a 4- or 8-microsecond delay between DMA bursts. Even with the DM01 adaptive DMA, some applications may require a longer burst delay to allow other devices adequate time on the bus.

Switch	OFF	ON	Factory
SW2-7	4 usec	8 usec	OFF

**4.3.3.5 DMA Adaptive Mode**

Depending on the other devices on the bus and their priority, the DM01 may use more or less bus time than optimal for your application. The DM01 allows you to modify its DMA operations by disabling adaptive DMA. If adaptive DMA is disabled, the host processor programs the DMA burst length to a maximum of 8 words per burst.

When adaptive DMA is enabled (SW2-8 OFF), the DM01 monitors the LSI-11 bus for other pending DMA requests and suspends its own DMA activity to permit other DMA transfers to occur. If the DM01 is not getting the bus time your application requires, you may want to disable the adaptive DMA. When adaptive DMA is disabled, the DM01 performs a burst transfer of 8 words or less, relinquishes the bus, then performs another DMA burst transfer.

Switch	OFF	ON	Factory
SW2-8	Enable Adaptive DMA	Disable Adaptive DMA	OFF

**NOTE**

If you are using the DM01 with adaptive DMA enabled in a MicroVAX II subsystem, be aware that the CPU module uses DMA requests to gain use of the bus to service device interrupts and may interfere with the DM01's bus access. You may consider disabling adaptive DMA for improved throughput.

---

**4.4 Physical Installation**

---

This section provides instruction for system preparation, slot selection, and mounting the DM01.

---

**4.4.1 System Preparation**

---

To prepare your CPU to accept the DM01, use the following procedures:

- MICRO/PDP-11 and MicroVAX I and II Preparation:
  1. Power down the system by switching OFF the main ac breaker.
  2. Remove the rear cover from the chassis so that the patch panel is exposed. The rear cover is held on by snap pads. Grasp the cover at the top and bottom, and pull straight back.
  3. Loosen the captive screws from the patch panel using a standard screwdriver.
  4. Remove the patch panel.
  5. Find the flat-ribbon cable that connects the CPU module to the patch panel. For easier board installation, you may disconnect the CPU flat-ribbon cable from the patch panel.
- LSI-11 Series Preparation:
  1. Power down the system by switching OFF the main ac breaker.
  2. Remove the cover from the chassis so that the backplane is exposed.

Do not replace the covers or patch panels until the installation is verified (subsection 4.9).

---

#### 4.4.2 Slot Selection

---

The DM01 may be assigned to any desired slot because it uses the LSI four-level interrupt scheme to perform distributed interrupt arbitration.

Be sure to find out whether your backplane is straight or serpentine and choose a slot accordingly. On straight backplanes, the DM01 must be plugged into connectors A and B, since connectors C and D carry no signals. On a serpentine backplane, the DM01 can be plugged into either connectors A and B or connectors C and D.

There must be no unused slots, however, between the CPU and the DM01. If you have a DEC RQDX3 in your backplane, be sure to install the DM01 in front of the RQDX3; not all RQDX3 controllers pass grant signals.

---

#### 4.4.3 Mounting

---

The DM01 Disk Controller PWB should be plugged into the LSI-11 backplane with components oriented in the same direction as the CPU and other modules. Always insert and remove the boards with the computer power OFF to avoid possible damage to the circuitry. Be sure that the board is properly positioned in the throat of the board guides before attempting to seat the board by means of the extractor handle.

---

### 4.5 ST-506 Disk Drive Preparation

---

There is only one configuration setup that needs to be made on common ST-506 disk drives: drive unit numbering.

Most ST-506 peripherals must have unit numbers assigned to them during installation. The DM01 supports drive unit numbers of only 0 and 1. Check the drive manual for instructions on how to set the unit drive number.

Select either 0 or 1 for your drives in accordance with the configuration to be programmed into the NOVRAM (see sections 4.8, 4.9, 4.10).

---

## **4.6 SA450 Floppy Drive Preparation**

---

Most SA450 floppy disk drive(s) have a number of jumper-selectable options. The DM01 requires that you select a drive unit number. Recommendations for these and other options are described in the following subsections.

---

### **4.6.1 Drive Numbering**

---

An address from 0 to 1 must be selected for each drive. Be careful that no two drives are assigned the same number. The logical unit number is determined by the address given to the drive. See subsection 3.2.2.

Most SA450 drive addresses are selected by jumper positions. Refer to the manufacturer's drive manual for the proper jumper positions.

---

### **4.6.2 Head Loading Conditions**

---

If the SA450 interface on the floppy drive uses HEAD LOAD on pin 4, the DM01 requires that the HEAD LOAD function be selected only when the DRIVE SELECT signal for that unit is true. Consult the appropriate drive manual for the exact procedure.

---

### 4.6.3 Motor On Conditions

---

The DM01 requires that the MOTOR ON function be true when DRIVE SELECT is true. The DM01 will operate with drives that must have both MOTOR ON true and DRIVE SELECT true for the MOTOR ON function. Consult the appropriate drive manual for the exact procedure.

---

### 4.6.4 Indicators

---

Because the DM01 does not support the IN USE option on pin 4 of the SA450 interface, configure the floppy drive so that the indicators are updated when DRIVE SELECT is true. Consult the appropriate drive manual for the exact procedure.

---

### 4.6.5 Pack Transition Support

---

The READY line (34) on an SA450 floppy drive interface may have several, optional definitions, according to the drive manufacturer. The READY line must be TRUE as a function of the floppy door being closed in order to support pack transition. The READY conditions are usually jumper selectable. The DM01 does not support pack transition notification with SA450 floppy drives that do not have the required READY line condition.

---

### 4.6.6 Termination

---

Terminator power is supplied by the drive. The terminated drive must have power applied in order for termination to be effective; otherwise, indeterminate results will occur.

Only the last drive in the string is terminated.

---

## 4.7 Cabling

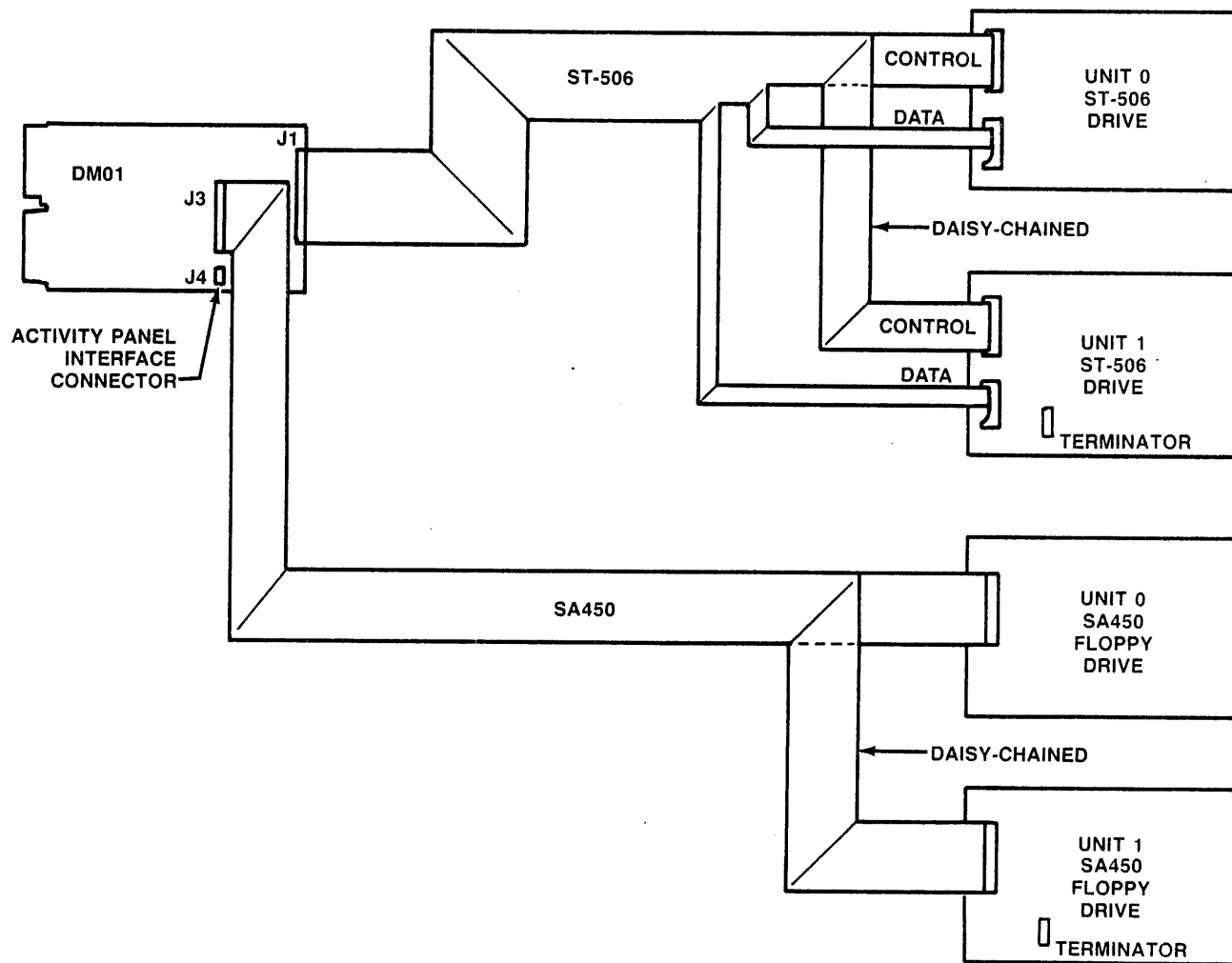
---

The DM01 Disk Controller controls its two physical hard disk drives via the ST-506 interface and the two floppy drives via the SA450 interface.

The ST-506 interface has two parts: a 34-line control segment, which is connected to both drives, and two 8-line data segments, one of which goes to each drive. The ST-506 physical interface is implemented by header J1 on the DM01 module. (For more information on pin/signal assignment, refer to section 8.3.) The control cable segment (J1, pins 1 through 34) is daisy-chained between the two disk drives. The data cable segments (J1, pins 35 through 42 for drive 0 and pins 43 through 50 for drive 1) each connect to one of the drives. Maximum cable length is 20 feet (6 meters).

The SA450 34-line signal interface is implemented by header J3 on the DM01 module. Maximum cable length is 10 feet (3 meters). (For more information on pin/signal assignment, refer to subsection 8.4.)

Emulex offers the DM01 Internal Cabling Kit (P/N DM0113001-00) which allows you to install the DM01, the floppy drive(s), and the hard disk drive(s) in the CPU cabinet. Figure 4-4 shows basic cable installation.



DM0102-0919

Figure 4-4. Drive Cabling



The Federal Communications Commission (FCC) has mandated that equipment that uses radio-frequency signals in its operation must limit the amount of electromagnetic interference (EMI) that it radiates. Most manufacturers, including DEC, limit EMI by building continuous metal shields into their equipment cabinets.

The routing of the cables that connect the DM01 and its disk drives can have a major impact on the amount of EMI that is radiated by the subsystem (the combination of the DM01 and its drives), especially if the DM01 and the disk drives are installed in separate cabinets.

When the DM01 and the disk drives are installed in the same cabinet, it is possible that the cabinet itself provides sufficient shielding. In such cases, it is not usually necessary to shield the cables that carry the interface between the DM01 and the peripherals.

#### NOTE

If the cabinet in which the DM01 and LSI-11 CPU are installed was manufactured before 1 October 1983, it may not provide sufficient shielding or filtering to prevent excessive RFI radiation or conduction. In case of complaint, it is the operator's responsibility to take whatever steps are necessary to correct the interference.

If the ST-506 disk drives are mounted in a separate cabinet from the DM01 Disk Controller and the SA450 floppy drive(s), then the cables that connect the DM01 to the drives should probably be shielded, because they run outside the shielded cabinet environment.

In addition, you should take special care that the integrity of the shield is maintained where the cables pass through it. Usually, designers use clamps that effectively connect the cable shielding to the cabinet shield.

---

## **4.8 NOVRAM Loading, Disk Formatting, and Testing**

---

After physically installing the DM01, several steps are required to prepare the subsystem for operation. They are:

- Loading the drive configuration into the NOVRAM
- Formatting and verifying the media
- Testing the subsystem

The DM01 disk controller firmware incorporates a self-contained set of disk preparation and diagnostic utilities, called firmware-resident diagnostics (F.R.D.). F.R.D. provides several important disk preparation functions, including the ability to configure the controller NOVRAM, format the drive, test the disk surface and replace defective blocks, and perform reliability testing of the attached disk subsystem. These utilities allow you to communicate directly with either CRT or hardcopy devices connected to an LSI-11 or MicroVAX console port.

The basic application of F.R.D. is in preparing MSCP disk drives for use in your subsystem. Before data can be stored on a drive, the disk must be formatted and any bad blocks identified. F.R.D. provides options that allow you to perform these functions. You use NOVRAM configuration options to set and review your drive parameter values.

The steps involved in disk preparation are formatting the drive and then verifying that each logical block is good.

Automatic replacement, or blanket bad block replacement, is a feature of several F.R.D. options. (Floppy drives are excluded from bad block replacement.) With this feature, you can format a drive, verify, and replace any bad blocks in one step. During this format/verify operation, bad blocks are displayed in logical block number (LBN) format. If replacement is enabled, the blocks are replaced automatically.

Manual bad block replacement is a separate option. This option allows you to identify specific bad blocks to be replaced. In addition, you can identify the blocks in Bytes From Index (BFI) format or in LBN format. Using BFI format eliminates the calculation required for LBN. This is most often useful in replacing blocks identified as bad in the manufacturer's defect list.

BFI replacement must be done before any LBN replacement. Once LBN replacement occurs, the BFI values are no longer valid.

There are several ways you can use F.R.D. options to format and verify your disk. The method you choose depends on whether you:

- Have formatted this disk
- Want to replace blocks using BFI or LBN information
- Want to preserve data on this disk

Each method is described below. The options listed are on the F.R.D. main menu. Use them in the order they are listed. (F.R.D. options are described in section 4.9.)

If this is the initial format of a disk and you want to replace the manufacturer's detected defects from the hardcopy list, use:

- Option 1, Format
- Option 7, Replace Block (using BFI format)
- Option 3, Verify (with replacement enabled)

If this disk is formatted and you want to preserve data and obtain a list of bad blocks, use:

- Option 4 Read Only Test (with replacement disabled)

---

### 4.8.1 F.R.D. Conventions

---

F.R.D. uses the following keyboard conventions:

<CR> required to terminate operator inputs

<Ctrl C> aborts the current operation and returns to the main menu

A delay of 10 seconds may occur between the <Ctrl C> and the next display. During some verify operations, the delay may be considerably longer because the abort is delayed until the successful completion of the current command. In this case, a screen message informs you of the delay.

In this section, operator responses to F.R.D. prompts appear in **bold print**. The symbols used in this section are listed below with their meanings:

<CR> carriage return key  
<LF> line feed key  
<Ctrl C> Ctrl key and the letter C pressed at the same time

---

### 4.8.2 Starting F.R.D. on a MicroVAX I

---

F.R.D. is started by issuing a special command sequence via console ODT. To start F.R.D. on a MicroVAX I, first apply power to the system. Put the system in console mode, then enter the following commands via the host console in response to the >>> prompt:

```
>>>I                                     !BUS INIT
>>>D/P/W 2000XXXX 1<CR>                 !INIT CONTROLLER
>>>E/P/W 2000YYYY<CR> 900 OR B00        !STEP ONE FROM DM01
>>>D/P/W 2000YYYY 3003<CR>              !ENABLE SPECIAL MODE
>>>E/P/W 2000YYYY<CR> 0100              !ACK SPECIAL MODE
>>>D/P/W 2000YYYY 4401<CR>              !UPLOAD DRIVER
>>>E/P/W 2000YYYY<CR> 0400              !UPLOAD DONE
>>>S      80<CR>                         !START DRIVER
```

NOTE: XXXX and YYYY are offsets dependent on the address of the DM01 controller. See Table 4-8 for the available values.

### 4.8.3 Starting F.R.D. on a MicroVAX II

F.R.D. is started by issuing a special command sequence via console ODT. To start F.R.D. on a MicroVAX II, first apply power to the system. Put the system in console mode, then enter the following commands via the host console in response to the >>> prompt:

```

>>>I                                !BUS INIT
>>>D/P/W 2001F40 20<CR>            !ENABLE MEMORY
>>>D/P/L 20088000 80000000<CR>     !SETUP MAP 0
>>>D/P/L 20088004 80000001<CR>     !SETUP MAP 1
>>>D/P/W 2000XXXX 1<CR>            !INIT CONTROLLER
>>>E/P/W 2000YYYY<CR> 900 OR B00   !STEP ONE FROM DM01
>>>D/P/W 2000YYYY 3003<CR>         !ENABLE SPECIAL MODE
>>>E/P/W 2000YYYY<CR> 0100        !ACK SPECIAL MODE
>>>D/P/W 2000YYYY 4401<CR>         !UPLOAD DRIVER
>>>E/P/W 2000YYYY<CR> 0400        !UPLOAD DONE
>>>S 80<CR>                        !START DRIVER

```

NOTE: XXXX and YYYY are offsets dependent on the address of the DM01 controller. See Table 4-6 for the available values.

Table 4-6. MicroVAX Offsets

CONTROLLER BUS ADDRESS	XXXX	YYYY
772150	1468	146A
772154	146C	146E
760334	00DC	00DE
760340	00E0	00E2
760344	00E4	00E6
760350	00E8	00EA
760354	00EC	00EE
760360	00F0	00F2

**4.8.4 Starting F.R.D. on an LSI-11 System**

To start F.R.D. on an LSI-11 system, first halt the processor. Then enter the following commands in response to the ODT prompt (@):

```
@ 177xxxx/ 000000 1 <LF>
@ 177yyyy/ 4400 or 5400 30003 <CR>
@/000400 42000 <CR>
@ 177yyyy/ 2000 <CR>           !TEST FOR 2000
@ 200G
```

NOTE: XXXX and YYY are offsets dependent on the address of the DM01 controller. See Table 4-7 for the available values.

Table 4-7. LSI-11 Offsets

CONTROLLER BUS ADDRESS	XXXX	YYYY
772150	72150	72152
772154	72154	72156
760334	60334	60336
760340	60340	60342
760344	60344	60366
760350	60350	60352
760354	60354	60356
760360	60360	60362

When the appropriate start procedure is completed, F.R.D. identifies itself by displaying the controller type and firmware revision. Then, it displays the menu options.

**4.8.5 Terminating F.R.D.**

To terminate F.R.D., choose one of the following:

- Press the BREAK key, or
- Reinitialize the system, or
- Halt the CPU.

You can restart the diagnostics from a halted condition if you have not changed the memory contents. On an LSI-11 system, enter 200G at the ODT prompt. On a MicroVAX system, enter S 80.

---

## 4.9 F.R.D. Options

---

F.R.D. is an interactive, menu-driven utility. This section describes the function of each option on the F.R.D. main menu. The menu appears as follows:

### Option Menu

- 1 - Format
- 2 - Format and verify
- 3 - Verify
- 4 - Read only test
- 5 - Data reliability test
- 6 - List known units
- 7 - Replace block
- 8 - Display Novram
- 9 - Edit / Load Novram

Enter option number:

The main menu and each submenu prompt for required input. When you enter a valid selection, the next menu displays or F.R.D. performs the selected option. If you make an invalid entry, F.R.D. rejects it, displays an error message, and reprompts.

Based on the nature of the MSCP emulation being performed, some operations may produce an observable delay when performed on previously unformatted drives. This delay is approximately 30 seconds.

When an option is finished, F.R.D. displays the prompt "Strike any key when ready" and waits for you to do so before returning to the main menu.

### NOTE

F.R.D. options that poll for a list of available drives print a drive list. If you are using one of these options and one of the drives cannot be spun up, F.R.D. can be suspended indefinitely. Use <CTRL C> to abort the operation.

---

### 4.9.1 Option 1 - Format

---

The Format option is used to initially format a drive. The operation writes sector headers and initializes the drive's RCT tables. It is used to format a virgin drive, a drive that has been determined to contain unusable data, or a drive with a format that is improper to use with a particular controller.

After formatting, the drive contains a valid RCT with a serial number you specified.

Floppy drives do not have RCT tables.

---

### **4.9.2 Option 2 - Format and Verify**

---

This option formats a drive, then tests the surface to replace pattern-sensitive defective sectors not found by the manufacturer. It performs both of the operations that are available separately with options 1 (Format) and 3 (Verify). This option also offers a bad block replacement feature which, when enabled, replaces any bad blocks, on an ST-506 drive only, found during the verify operation.

---

### **4.9.3 Option 3 - Verify**

---

The Verify option Write/Read exercises all user-available blocks. F.R.D. uses four worst-case data patterns to find and replace pattern-sensitive blocks not contained in the drive defect list. It asks for the logical unit number (LUN) of the drive to be verified. After you enter the LUN, F.R.D. prompts for the number of write/read passes.

Verify operations are performed on 120 logical blocks at a time. Logical blocks are referenced by logical block number (LBN).

The Verify option also offers a bad block replacement feature which, when enabled, replaces any bad blocks on an ST-506 drive, using the appropriate technique.

During Verify operations, F.R.D. disables all controller error recovery capabilities so that a sector is replaced for any repeatable single bit error. When a block is encountered that cannot be accessed because of header or data field errors, the Logical Block Number in error displays. Because the failing pattern may not be the first pattern, it is possible that replacement blocks may not be tested with all patterns. For this reason, Emulex recommends running at least two Verify passes over all four data patterns.

A floppy drive will report bad blocks but not replace them.



---

**4.9.4 Option 4 - Read Only Test**

---

This option causes all the user-available blocks on the selected drive to be Read-only, not Write/Read, during the Verify pass. When a block is encountered that cannot be accessed because of header or data field errors, the utility displays the Logical Block Number.

The Read Only Test option also offers a bad block replacement feature which, when enabled, replaces any bad blocks on an ST-506 drive. Because F.R.D. runs with ECC disabled and does not cache any read data, no corrected data is available to put in the replacement block. This means that even though the defective block is replaced and no forced error flag is set in the replacement sector, the data is nonvalid.

**CAUTION**

This may cause problems if the replaced blocks contain executable program files. For this reason, you should back up sensitive data before running this option with the replacement feature enabled.

This option is usually used after the drive is formatted. However, if you plan to manually replace the bad blocks identified in the manufacturer's defect list, be certain to do so before using Option 4 with replace enabled.

---

**4.9.5 Option 5 - Data Reliability Test**

---

This option allows you to thoroughly test your subsystem. The reliability test uses Write, Write/Check, and Read functions to test the controller-to-drive portion of the subsystem. In addition, an independent DMA operation between the host memory and the controller tests the host/controller interface.

For ST-506 drives, the test defaults to two reserved diagnostic cylinders so that user data will be protected; a test of the full pack is your option. To run the reliability test indefinitely, select 0 (zero) passes. Floppy drives test the entire media.

If the test encounters errors, F.R.D. displays text error messages. These messages are primarily for use by Emulex technical support personnel.

---

#### **4.9.6 Option 6 - List Known Units**

---

This option causes the program to list all the drives that are configured in the NOVRAM. Only those units that can be selected by the controller are listed as available.

A user size (in 512-byte blocks) and a media type I.D. are listed with all drives found by this option. The user size does not include RCT area, diagnostic cylinders, designated or hidden spare tracks or blocks, etc.

In addition, this option displays the attached drive's physical geometry. This display includes all areas of the disk. If the device size in logical blocks is calculated from this data, the number will be larger than the displayed user size. The difference is the number of LBNs used for RCT, diagnostic cylinders, spares, etc.

---

#### **4.9.7 Option 7 - Replace Block**

---

This option allows you to replace a specific bad block or group of blocks without using the blanket replacement feature found in the Verify and Read Only options. You choose to identify either logical blocks (entered in decimal MSCP Logical Block Number format) or Bytes From Index (as listed in the manufacturer's defect list), then enter the block to be replaced. This option is not supported on floppy drives.

BFI replacement eliminates the calculation required to translate BFI to LBN format. F.R.D. requires the cylinder, track, and bytes from index of the defect for each BFI entry. When you initiate replacement, F.R.D. prompts for the number of bytes per track. As soon as you enter this value, F.R.D. begins replacing blocks.

LBN replacement allows you to replace blocks identified as bad during the format operation, when they are identified in LBN format by older versions of DEC operating systems which do not support host-initiated replacement.

If you are using both types of replacement, BFI replacement must be complete before LBN replacement is begun. Further, BFI replacement must be complete before the blanket bad block replacement feature of other options is enabled.

Emulex recommends that you run the Verify option after the replacement option is complete. That runs test patterns that may detect any pattern-sensitive blocks.

---

#### **4.9.8 Option 8 - Display NOVRAM**

---

This option displays the current contents of the NOVRAM for your drives. The information displayed depends on the type code entered in the NOVRAM. For type 1 drives, this option displays the current NOVRAM parameter values. For drive types 2, 3, 4 or 5, floppy parameters will be displayed.

---

#### **4.9.9 Option 9 - Edit/Load NOVRAM**

---

This option allows you to enter the drive configuration parameters into the controller. If a drive type code of 1 is specified, F.R.D. prompts you for the required drive parameters. For drive types 2, 3, 4 or 5, floppy parameters will be displayed.

---

### **4.10 Drive Configuration Parameters**

---

When you edit or load NOVRAM configuration parameters, you are asked to enter the values required for your configuration. This section describes each parameter and states the range of valid entries for each. The required values for each drive supported by Emulex are listed in Appendix C.

You begin loading NOVRAM parameter values by selecting Option 9 from the F.R.D. main menu. F.R.D. then displays each parameter, one at a time. The parameter displays with a range of valid entries and a default value. Enter the appropriate value (in decimal) or simply press the return key to accept the default value (the last value entered). The next parameter then displays.

---

#### **4.10.1 Type Code**

---

This parameter indicates where the controller looks for drive configuration information. Valid values are 1 through 5. If you enter 1 - 5, the controller expects to find drive configuration information contained in the NOVDRAM. F.R.D. then displays each parameter for you to enter the values.

Type codes and their description follow:

Type	Description
1	ST-506
2	Single-sided, double-density floppy, RX50-compatible, pack transition not supported
3	Double-sided, double-density floppy, not compatible with the RX50, pack transition not supported
4	Single-sided, double-density floppy, RX50-compatible, pack transition supported
5	Double-sided, double-density floppy, not compatible with the RX50, pack transition supported

---

#### **4.10.2 Number of Units of this Type**

---

This parameter specifies the quantity of attached physical disk drives that use the NOVDRAM parameters that follow. Valid values are 1 and 2. If you enter 1, the utility uses a separate set of parameter values for each drive. In this case, it prompts for parameter values for the second drive. If you enter 2, the same parameter values are used for both drives.

---

#### **4.10.3 Starting Head Offset**

---

This parameter specifies the physical drive head that is to be used as the first head of the second logical drive. For hard disks, this field has meaning only if a Split Code of 3 is specified. For example, if a split code of 3 was specified and the Starting Head Offset was set to 2 for a drive with four heads, then heads 0 and 1 would be used for the first logical drive and 2 and 3 would be used for the second logical drive. This is only used for Type Code 1 drives.

---

**4.10.4 Number of Sectors per Track**

---

For Type 1 drives, this parameter specifies the total number of physical sectors per track, including spares. The valid range is from 1 through 63. This value is used only for type code 1.

---

**4.10.5 Number of Heads**

---

For Type 1 drives, this parameter specifies the number of data heads per physical drive. The valid range is from 1 through 16.

---

**4.10.6 Number of Cylinders**

---

For Type 1 drives, this parameter specifies the total number of physical cylinders per drive, including spares. The valid range is from 1 through 4,095.

---

**4.10.7 Number of Spare Sectors per Track**

---

For Type 1 drives, this parameter specifies the number of spare sectors reserved per track. Emulex recommends a value of 1; larger values will unnecessarily reduce the capacity of the drive.

---

**4.10.8 Number of Alternate Cylinders**

---

For Type 1 drives, this parameter specifies the number of spare cylinders per physical drive. The valid range is from 0 through 127. You must reserve two cylinders if spare sectors are specified (the sector replacement algorithm needs the cylinders for working space). A minimum of two cylinders must be specified as alternates. Emulex recommends a value of two.

If Split Code 1 or 2 is used, alternate cylinders are divided evenly between the two logical drives. Therefore, an even number of alternate cylinders should be specified. If Split Code 3 is specified, the alternate cylinders are divided on a per head basis (as the logical units are). A minimum of 2 alternate cylinders must be specified if block replacement is to function with a cylinder split. This value is used only for type 1 drives.

---

### 4.10.9 Split Code

---

For type 1 drives, this value allows the drive defined by this parameter to be split into two logical disk units (two each, if more than one drive is defined by this parameter). The relative sizes of the logical drives are defined as follows:

Code	Drive 0	Drive 1
0	8/8	0
1	4/8	4/8
2	7/8	1/8
3	Head Offset Dependent	

**Code 0:** No split.

**Code 1:** The cylinders are split evenly between the two logical drives. If there is an odd number of cylinders, the odd cylinder is not used.

**Code 2:** The total number of cylinders is divided by 8. The first drive contains seven-eighths of the total, and the second contains one-eighth. When the number of cylinders is not evenly divisible by eight, the odd cylinders (remainder of the division) are assigned to the larger logical unit.

**Code 3:** The drive's data heads are divided between the two logical drives.

Use of the split option disables seek-ordering and overlapped seek processing in the MSCP Controller, which reduces performance, particularly when both logicals of a split physical drive are active.

---

### 4.10.10 Removable Media

---

For type 1 drives, this parameter indicates whether the disk media is fixed or removable. If you are defining one logical/physical drive, this parameter uses a 1-bit field with valid values of 0 and 1, where 0 indicates fixed media and 1 indicates removable media.

If you are defining a drive with a logical split, this parameter uses a 2-bit field with a valid range from 0 through 3:

Definition	Decimal Value
LUN 0 and LUN 1 are both fixed.	0
LUN 0 is removable, LUN 1 is fixed.	1
LUN 0 is fixed, LUN 1 is removable.	2
LUN 0 and LUN 1 are both removable.	3

---

**4.10.11 Hard/Soft Sector Flag**

---

For type 1 drives, this parameter indicates whether the disk drive is hard or soft sectored. As all ST-506 drives are currently soft sectored, the DM01 always interprets this value as 0. No other values are valid.

---

**4.10.12 Reduced Write Current Cylinder**

---

For type 1 drives, this parameter specifies the physical cylinder at which the write current to the data heads is reduced. Some disk drives require that the write current to the heads be reduced above a certain cylinder to reduce the strength of the flux transition. This reduction prevents adjacent flux transitions in the higher cylinders (where they are closer together) from displacing one another to such an extent as to force data bits out of their data clock windows.

Consult the drive manufacturer's technical manual for the proper cylinder. The valid range is from 0 through 4096. If no reduction is required, specify the total number of physical cylinders.

---

**4.10.13 Write Precompensation Cylinder**

---

For type 1 drives, this parameter specifies the physical cylinder at which the timing of write data transmitted to the disk drive must be advanced or retarded (with reference to the disk data clock). This timing shift compensates for timing shifts that are caused by adjacent flux transitions in the higher cylinders (where they are closer together). Shifting the write data with respect to the data clock ensures that, when the data is read back, the data will fall within the clock window, despite the tendency of one transition to affect the apparent position of the adjacent transition. This shifting is called precompensation.

Consult the drive manufacturer's technical manual for the proper cylinder. The valid range is from 0 through 4096. If no precompensation is required, specify the total number of physical cylinders.

Write data is precompensated by 12 nanoseconds (both early and late).

---

#### 4.10.14 Step Pulse for Floppy Drives

---

For floppy drive type 1, this parameter is used to specify one of these head stepping codes:

Code	Stepping	
	Timing	Technique
0	3 msec	Nonbuffered
1	13.2 usec	Buffered
2	30 usec	Buffered
3	Reserved	

For type 2, 3, 4, 5 drives, this parameter is used to specify one of these head stepping codes:

Code	Stepping Timing
0	3 msec
1	6 msec
2	10 msec
3	15 msec

**Timing:** This number indicates the rate at which step pulses are transmitted to the disk drive.

**Technique:** This term indicates whether or not the DM01 expects the hard disk drive to buffer step pulses (i.e., to accept pulses faster than it can execute them). Actually, timing is what is being specified, but because few drives can seek faster than 3 msec cylinder-to-cylinder, shorter pulse intervals assume that the drive is buffering pulses.

---

#### 4.10.15 Spiral Offset or Sector Zero Offset

---

This parameter specifies the number of sectors by which sector 0 of a track is offset from sector 0 of the previous track. Offsetting sector 0 from one track to the next is a technique that is used to reduce latency when performing write or read operations that cross a track boundary. When the drive is formatted, sector 0 of a track is offset a certain number of sectors from the position of sector 0 on the previous track. When this is done, spiral write and read operations are more efficient because the drive has time to seek from track to track before encountering sector 0.

The valid range is from 0 through 31.



---

## 4.11 Operation

---

There are no operational instructions. The DM01 is ready for MSCP initialization as soon as its drives are formatted and tested.

Pack transition support requires attention to some operational conditions:

- If a diskette is changed while the floppy drive select light is ON, the DM01 is not able to detect the new diskette and completes the disk operation on the new media.
- To support pack transition, the DM01 polls the READY line every 5 seconds on floppy drives that are not busy. Diskettes that are changed in less than 5 seconds (unlikely) would not register a pack transition notification.

---

### 4.11.1 Indicators

---

There are three light emitting diodes (LEDs) on the DM01 PWB. These LEDs are used for both diagnostics and for normal operations.

If switch SW2-1 is OFF, the DM01 executes a preliminary test at the following times:

- On power-up
- After a reset condition
- After a bus initialization
- After a write operation to the Initialization and Polling (IP) register (base address)

The self-test routine consists of two test sequences: preliminary and self-test. The preliminary test sequence exercises the 8031 microprocessor chip and the Disk Formatter chip. When the DM01 successfully completes the preliminary test, LED3 illuminates indicating that the DM01 is waiting for the MSCP initialization sequence.

During the MSCP initialization sequence, initiated by host software control, the DM01 executes a self-test that exercises the buffer controller chip, the Host Adapter Controller (HAC) chip and its associated circuitry, the on-board RAM, and the control memory PROM. If the DM01 passes this sequence of its self-test successfully, all the LED indicators on the edge of the DM01 are OFF.

If a fatal error is detected either during self-test or while the system is running, all three of the edge-mounted LED indicators are ON (illuminated). If the DM01 fails to pass its power-up self-tests, you can select a special diagnostic mode (switch SW2-1 ON) which causes the LED indicators to display an error code. See Self-Test Error Reporting, in Section 5, TROUBLESHOOTING.

During normal operation, LED1 and LED2 flicker occasionally. These LEDs are used to indicate LSI-11 bus activity and ST-506 disk drive activity, respectively.



---

## **5.1 Overview**

---

This section describes the several diagnostic features with which the DM01 Disk Controller is equipped, and outlines fault isolation procedures that use these diagnostic features.

Subsection	Title
5.2	Service
5.3	Fault Isolation Procedure
5.4	Power-Up Self-Diagnostics
5.5	Fatal Error Codes

---

## **5.2 Service**

---

Your Emulex DM01 Disk Controller was designed to give years of trouble-free service, and it was thoroughly tested before leaving the factory.

Should one of the fault isolation procedures indicate that the DM01 is not working properly, the product must be returned to the factory or to one of Emulex's authorized repair centers for service. Emulex products are not designed to be repaired in the field.

Before returning the product to Emulex, whether the product is under warranty or not, you must contact the factory or the factory's representative for instructions and a Return Materials Authorization (RMA) number.

**Do not return a component to EMULEX without authorization.** A component returned for service without an authorization will be returned to the owner at the owner's expense.

In the continental United States, Alaska, and Hawaii contact:

Emulex Technical Support  
3545 Harbor Boulevard  
Costa Mesa, CA 92626  
(714)662-5600 TWX 910-595-2521  
Outside California: (800) 854-7112

After 5 p.m. Pacific Time, call (800) 638-7243. When answered, you will be prompted to key in 37115, followed by a # symbol, then a message.

Outside the United States, contact the distributor from whom the subsystem was initially purchased.

To help you efficiently, Emulex or its representative requires certain information about the product and the environment in which it is installed. During installation, a record of the switch setting should have been made on the Configuration Reference Sheet. This sheet is contained in the Installation Section, Figure 4-1.

After you have contacted Emulex and received an RMA, package the component (preferably using the original packing material) and send the component **postage paid** to the address given you by the Emulex representative. The sender must also insure the package.

---


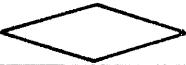


### **5.3 Fault Isolation Procedure**

---

This fault isolation procedure is provided in flow chart format. The procedure is based on the self-diagnostics incorporated into the DM01. The procedure is designed to be used if the product's self-diagnostic fails or if many errors are flagged by the subsystem during normal operation. If neither of these events happens, it is not necessary to follow these procedures.

The Fault Isolation Chart is shown in Figure 5-1. The chart symbols are defined in Table 5-1.

Table 5-1. Flow Chart Symbol Definitions

<b>Symbol</b>	<b>Description</b>
	Start point, ending point.
	Decision, go ahead according with YES or NO.
	Connector, go to same-numbered symbol on another sheet.
	Process.

DM0102-0106

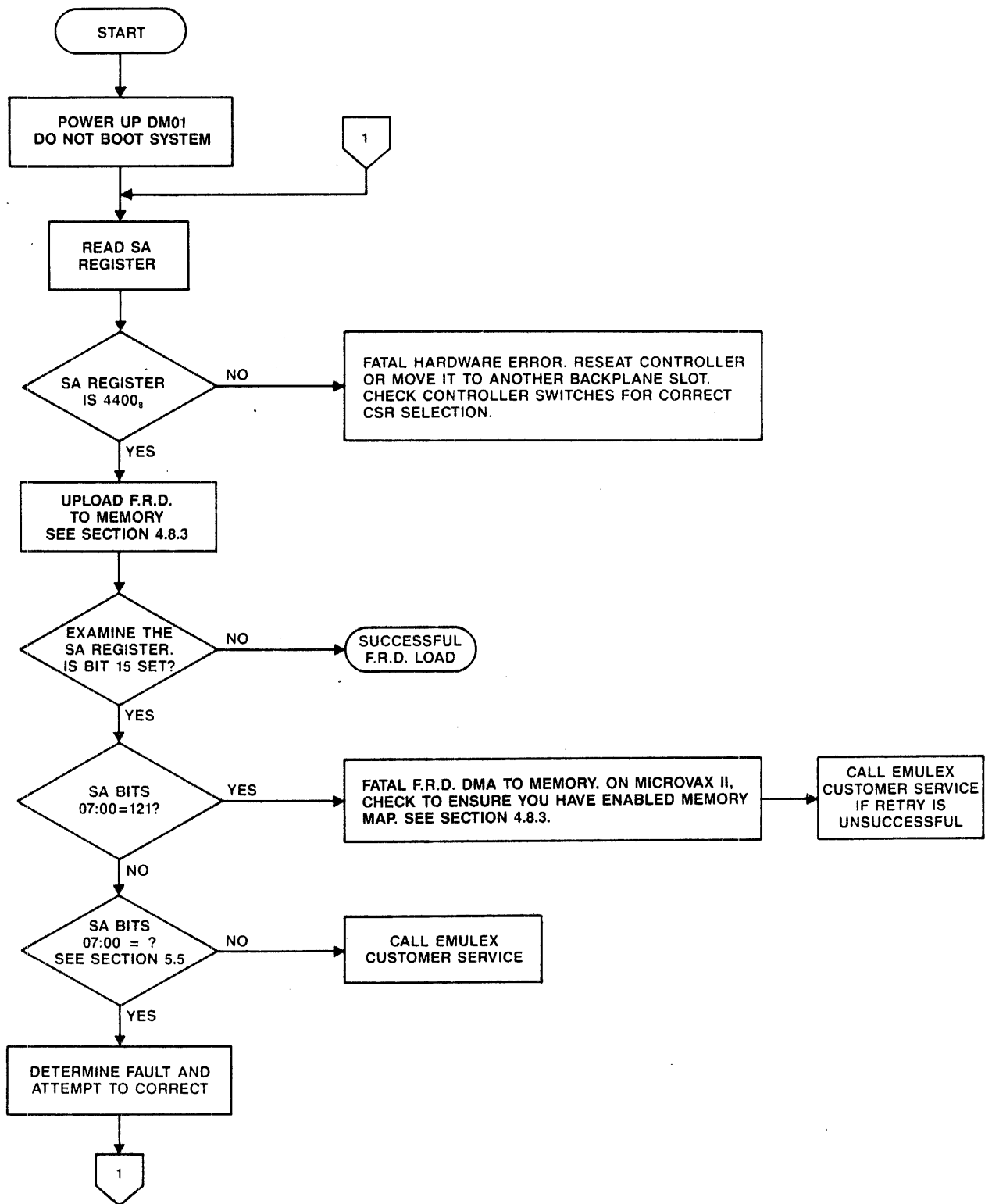


Figure 5-1. Fault Isolation Chart

## 5.4 Power-Up Self-Diagnostic

---

DM01 executes an extensive self-diagnostic to ensure that the disk controller is in good working order. The self-diagnostic is divided into several parts. Table 5-2 lists the tests in the order in which they are performed.

The first two tests are executed immediately after power-up, a reset, a bus INIT, or a write to the IP register (base address). The other tests are executed as the controller interacts with the MSCP initialization routine. If the DM01 fails any of the tests, it posts an MSCP fatal error code in the low-byte of the SA register (base address plus 2) and turns on three LEDs which are located on the outside edge of the PWB. The MSCP fatal error codes used by the DM01 are listed in Table 5-3. Note that some fatal errors may not allow access to the SA register from the console.

To help determine the location of the problem, the operator can select a special diagnostic mode that causes the LEDs to display an error code. To enable this diagnostic mode, place the CPU halt switch in the ON position and set DM01 switch SW2-1 ON (1). After setting SW2-1 ON, the host computer must be powered down or DM01 switch SW1-1 must be toggled (turned ON and then OFF) to cause the DM01 to again perform its self-test.

Upon encountering an error, the host microprocessor halts and the LEDs display an error code. The error codes are listed and described in Table 5-2.

If the DM01 completes the diagnostic mode without errors, all three LEDs are OFF. Set switch SW2-1 in the OFF position and reset the DM01 controller before using.

Table 5-2. LED Error Codes

LED			Error Description
3	2	1	
0	0	0	Self-Diagnostic complete without errors
0	0	1	CPU Chip Test failed
0	1	0	Formatter Chip Test failed
1	0	0	Controller idle, waiting for initialization
0	1	1	Buffer Controller or External Memory Test failed
1	0	1	HAC Test failed
1	1	0	Emulation PROM Checksum Test failed

## 5.5 Fatal Error Codes

If the DM01 detects a fatal error anytime during operation, all three LEDs are illuminated and an error code is posted in the low byte of the SA register (base address plus 2). Table 5-3 lists the MSCP fatal error codes used by the DM01.

Table 5-3. MSCP Fatal Error Codes Used by the DM01

Octal Code	Hex Code	Description
0	0	No information in message packet.
1	1	Possible parity or timeout error when the DM01 attempted to read data from a message packet.
2	2	Possible parity or timeout error when the DM01 attempted to write data to a message packet.
4	4	DM01 diagnostic self-test indicated a controller RAM error.
5	5	DM01 diagnostic self-test indicated a firmware checksum error.
6	6	Possible parity or timeout error when the DM01 attempted to read an envelope address from a command ring.
7	7	Possible parity or timeout error when the DM01 attempted to write an envelope address to a command ring.
11	9	Host did not communicate with DM01 within the time frame established while bringing the controller online.
12	A	Operating system sent more commands to the DM01 than the controller can accept.
13	B	Controller unable to perform DMA transfer operation correctly.
14	C	DM01 diagnostic self-test indicated controller fatal error.
16	E	The MSCP connection identifier is invalid.
23	13	An error occurred during the MSCP initialization sequence.

Additional fatal error messages may appear. These error codes are listed in Table 5-4.

Table 5-4. Fatal Error Codes

<b>Octal Code</b>	<b>Hex Code</b>	<b>Description</b>
004	04	RAM error
005	05	Firmware checksum error
014	0C	Fatal error during self-test
111	49	Autoboot timeout
121	51	F.R.D. load to memory failed



---

## **6.1 Overview**

---

This section contains an overview of the DM01 device registers that are accessible to the LSI-11 bus and that are used to monitor and control the DM01 Disk Controller. The registers are functionally compatible with DEC implementations of MSCP controllers.

The following table outlines the contents of this section.

Subsection	Title
6.2	Overview of MSCP Subsystem
6.3	Programming
6.4	Registers
6.5	Bootstrap Command

---

## **6.2 Overview of MSCP Subsystem**

---

Mass Storage Control Protocol (MSCP) is the protocol used by a family of mass storage controllers and devices designed and built by Digital Equipment Corporation. MSCP allows a host system to be connected to subsystems with a variety of capacities and geometries. This flexibility is possible because MSCP defines data locations in terms of sequential, logical blocks, not in terms of a physical description of the data's location (i.e., cylinder, track, and sector). This scheme gives the MSCP subsystem the responsibility for converting MSCP logical block numbers into physical addresses that the peripheral device can understand.

This technique has several implications. First, the MSCP subsystem must have detailed knowledge of the peripheral's capacity, geometry, and status. Second, the ability to make the translation between logical and physical addresses implies considerable intelligence on the part of the subsystem. Finally, the host is relieved of responsibility for error detection and correction because its knowledge of the media is insufficient to allow error control to be done efficiently.

There are several advantages to this type of architecture. First, it provides the host with an "error-free" media. Second, it provides for exceptional operating system software portability because, with the exception of capacity, the characteristics of all MSCP subsystems are the same from the operating system's point of view.

In terms of implementation, this protocol requires a high degree of intelligence on the part of the subsystem. Essentially, this intelligence is a process that runs on a microprocessor and is referred to as an MSCP controller. The MSCP controller has all of the responsibilities outlined above.

The host computer runs corresponding software processes which take calls from the operating system, convert them into MSCP commands, and cause the resulting command to be transferred to the MSCP controller.

In summary, an MSCP subsystem is characterized by an intelligent controller that provides the host with the view of a perfect media. It is further characterized by host independence from a specific bus, controller, or device type.

For more information about MSCP subsystems, see subsections 3.2, 3.3, and 3.4.

---

## **6.3 Programming**

---

A complete description of MSCP commands and the corresponding status responses which the DM01 Disk Controller posts is beyond the scope of this manual.

---

### **6.3.1 MSCP Command Support**

---

No currently available MSCP Controller supports the entire range of MSCP commands. The following subsections describe the extent of MSCP command support by the DM01.

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#### **6.3.1.1 Minimal Disk Subset**

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The DM01 Disk Controller supports the entire minimal disk subset of MSCP commands.

---

#### **6.3.1.2 Diagnostic and Utility Protocol (DUP)**

---

The DM01 Disk Controller does not support any of the DUP commands or maintenance read/write commands. Therefore, the DM01 is not compatible with DEC diagnostics that use the MSCP DUP commands.

---

## 6.4 Registers

---

During normal operation, the DM01 Disk Controller is controlled and monitored using the command and status packets that are exchanged by the Class Driver (host) and the MSCP Controller. The DM01 has two 16-bit registers in the LSI-11 Bus I/O page that are used primarily to initialize the subsystem. During normal operation, the registers are used only to initiate polling or to reset the subsystem. These registers are always read as words. The register pair begins on a longword boundary. Table 6-1 lists the octal and hexadecimal values for the Initialization and Polling (IP) register (base address) and the Status and Address (SA) register (base address plus 2) supported by the DM01.

The IP register (base address) has two functions as detailed below:

- When written with any value, it causes a hard initialization of the MSCP Controller.
- When read while the port is operating, it causes the controller to initiate polling.

The SA register (base address plus 2) has four functions as listed below:

- When read by the host during initialization, it communicates data and error information relating to the initialization process.
- When written by the host during initialization, it communicates certain host-specific parameters to the port.
- When read by the host during normal operation, it communicates status information including port and controller-detected fatal errors.
- When zeroed by the host during either initialization or normal operation, it signals the port that the host has successfully completed a bus adapter purge in response to a port-initiated purge request.

Table 6-1. DM01 IP and SA Registers

Register	Octal	Hexadecimal
IP SA	772150 772152	20001468 2000146A
IP SA	772154 772156	2000146C 2000146E
IP SA	760334 760336	200000DC 200000DE
IP SA	760340 760342	200000E0 200000E2
IP SA	760344 760346	200000E4 200000E6
IP SA	760350 760352	200000E8 200000EA
IP SA	760354 760356	200000EC 200000EE
IP SA	760360 760362	200000F0 200000F2

---

## 6.5 Bootstrap Command

---

To allow the system to be easily bootstrapped from peripherals attached to the DM01 Disk Controller, Emulex has incorporated a Bootstrap Command into the controller. This feature is not part of the standard MSCP command set nor is it supported on the MicroVAX or on systems using an 11/73B CPU module.

The Bootstrap Command can be issued from the console after the system is powered up, or it may be incorporated into a firmware routine that is located in a Bootstrap ROM. (The ROM would not be located on the DM01 PWB, but on some other module in the system.) The Bootstrap Command causes the DM01 to load the first logical block from the selected peripheral into host memory starting at location 00000.

To issue the Bootstrap Command to the DM01:

1. Initialize the DM01 by writing any value into the IP register (base address). The DM01 performs self-test and begins the initialization dialog.

Register	Octal
IP: Write	000001

2. The DM01 indicates that initialization step 1 has begun by setting bit 11 in the SA register (base address plus 2). The host must poll the register for this value (no interrupt is generated). Bit 8 should also be set. If 22-bit addressing is enabled, bit 9 will be set.

Register	Octal	Addressing
SA: Read	004400	18-Bit
	005400	22-Bit

3. When the controller indicates that step 1 of the initialization dialog is begun, load the SA register (base address plus 2) with the "special initialization code:"

Register	Octal
SA: Write	030003

4. The controller acknowledges the initialization code with 00400.

Register	Octal
SA: Read	000400

5. Load the SA register (base address plus 2) with  $04000n_8$  or  $400n_{16}$ , where  $n$  is the MSCP logical unit number of the unit to bootstrap from. In this example, the unit is 0.

Register	Octal
SA: Write	040000

6. At the console emulator prompt, deposit the unit number in Register 0 (R0). Load the base address of the DM01 in Register 1 (R1). Execute the boot code by entering:

@0G



---

## **7.1 Overview**

---

This section contains a description of the DM01 Disk Controller's architecture.

---

## **7.2 DM01 Disk Controller Architecture**

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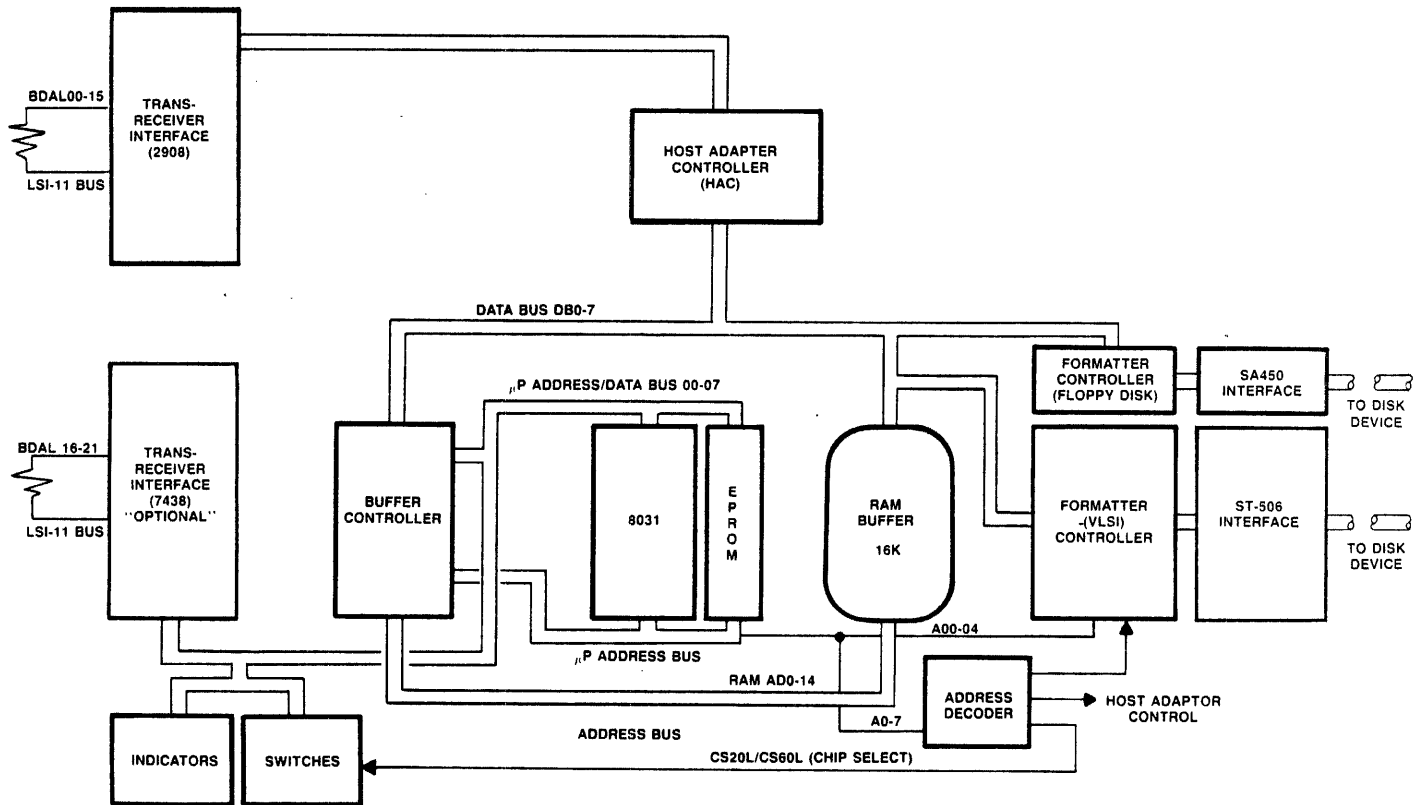
The DM01 is a microprocessor-based emulating disk controller that is contained on a single dual-wide PCBA. The DM01's major functional blocks are shown in Figure 7-1. The disk controller is organized around the eight-bit 8031 microprocessor. The board has an eight-bit internal data bus with 16-bit addressing capability. The Host Adapter Controller, the Hard Disk Formatter Controller, the Floppy Disk Formatter Controller, and the Buffer Controller are addressed as memory (memory-mapped I/O).

The 8031's primary task is to decode and implement commands from the host. At command completion, the microprocessor is also responsible for generating status and transmitting it to the host. A large part of the microprocessor's job while performing those duties involves setting up the Host Adapter Controller and the Buffer Controller for the large data transfers that are their specialties.

The DM01 uses a 27256 erasable programmable read-only memory (EPROM), which contains the control program, and 32K bytes of random access memory (RAM), which is used for data buffering and working storage.

The LSI-11 bus interface contains 22 lines. Sixteen of the lines are multiplexed for both address and data; six are used for only address. The Host Adapter Controller is used for programmed I/O, CPU interrupts, and DMA data transfers. The microprocessor responds to all programmed I/O and carries out the I/O functions required for the addressed disk controller register. The Host Adapter Controller has automatic LSI-11 bus address generation capability that, in conjunction with a byte counter, allows the interface to conduct LSI-11 bus DMA transfers without direct microprocessor intervention after the interface is set up for a transfer. This automatic DMA capability is used with the DM01 Buffer Controller to transfer large blocks of data directly between host memory and the DM01's RAM.

The Buffer Controller is implemented on a single chip. This multichannel DMA is responsible for moving large blocks of data between the 16K RAM buffer and the ST-506 and SA450 interfaces, and between the LSI-11 bus interface and the 16K RAM buffer. After being set up for an operation by the microprocessor, either interface requests DMA service from the Buffer Controller by driving an individual request signal active. The transfer then proceeds without direct intervention by the microprocessor. This allows high-speed data transfers to occur while the microprocessor is focused on other processes.



DM0102-0633

Figure 7-1. DM01 Block Diagram



---

## 8.1 Overview

---

This section describes the interfaces that the DM01 Disk Controller incorporates. It includes information on the DM01 implementation of ST-506 and SA450 interface electrical and mechanical requirements. Excluding this overview, the section is divided into the following subsections.

Subsection	Title
8.2	DM01 LSI-11 Bus Interface
8.3	DM01 ST-506 Drive Interface
8.4	DM01 SA450 Floppy Drive Interface

---

## 8.2 LSI-11 Bus Interface

---

The LSI-11 bus between the CPU and the DM01 Disk Controller contains 42 bidirectional signal lines and two unidirectional signal lines on connectors A and B. LSI-11 bus interface pin assignments are listed and described in Table 8-1. These signal lines provide the means by which the CPU and the DM01 Disk Controller communicate with each other.

The LSI-11 bus interface is used for programmed I/O, CPU interrupts, and DMA data transfer operations. Addresses, data, and control information are sent along these signal lines, some of which contain time-multiplexed information. The LSI-11 bus interface lines are grouped in the following categories:

- **Twenty-two Address Lines** <BDAL00:BDAL21>. Of these, 16 are multiplexed data/address lines <BDAL15:BDAL00>; two are multiplexed address/parity lines <BDAL17:BDAL16>; and four are extended address lines <BDAL21:BDAL18>.
- **Six Data Transfer Control Lines.** BBS7, BDIN, BDOUT, BRPLY, BSYNC, and BWTBT.
- **Ten Interrupt Control and Direct Memory Access Control Lines.** BIAKO, BIAKI, BIRQ4, BIRQ5, BIRQ6, BIRQ7, BDMGO, BDMGR, BSACK, and BDMGI.
- **Six System Control Lines.** BDCOK, BHALT, BINIT, BPOK, BEVNT, and BREF.



---

### 8.2.1 Interrupt Priority Level

---

The DM01 is hard-wired to issue level 4 interrupt requests and monitor level 5. The level 4 request is necessary to allow compatibility with either an LSI-11 or LSI-11/2 CPU.

---

### 8.2.2 Register Address

---

The DM01 Disk Controller has two registers visible to the LSI-11 bus. Their addresses are determined by DIP switches SW2-3 through SW2-5. See Section 4 for detailed address and switch setting information.

---

### 8.2.3 DMA Operations

---

All DMA data transfer operations are performed under microprocessor control. When doing a Read From Memory operation, a check is made for memory parity or nonexistent memory (NXM) errors; during Write operation a check is made for NXM errors. If an error is detected, an MSCP status error is returned.

---

### 8.2.4 Scatter/Gather

---

The DM01 Disk Controller supports the MicroVAX I I/O technique of scatter-write operations and gather-read operations.

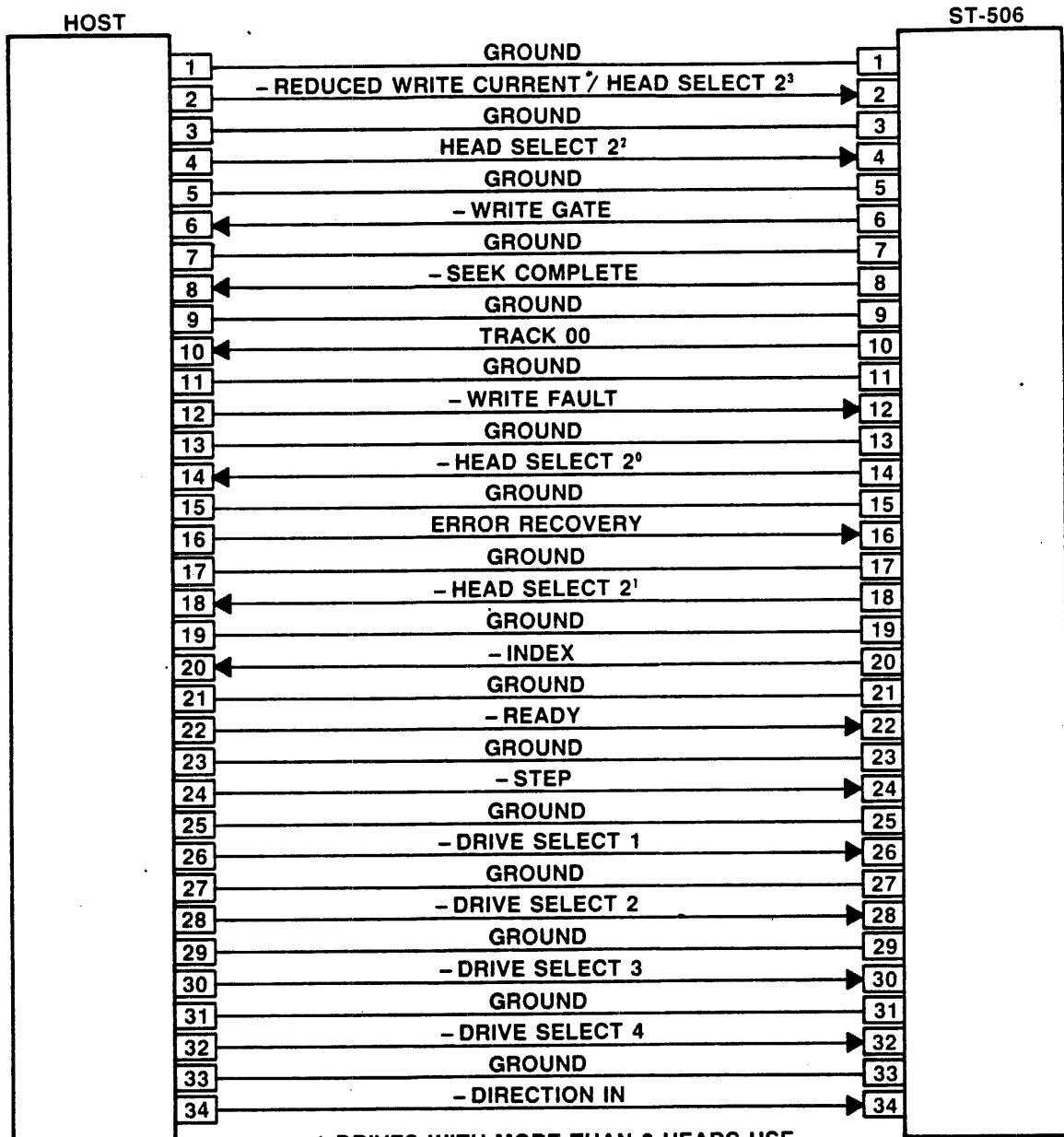
---

## 8.3 DM01 ST-506 Disk Drive Interface

---

The DM01 Controller interfaces with each ST-506 disk drive via a 34-pin control cable and one of two 8-pin data cables. A 50-pin male connector at reference designator J1 on the DM01 controller splits into an ST-506 control segment of 34-pins and two data segments of 8-pins.

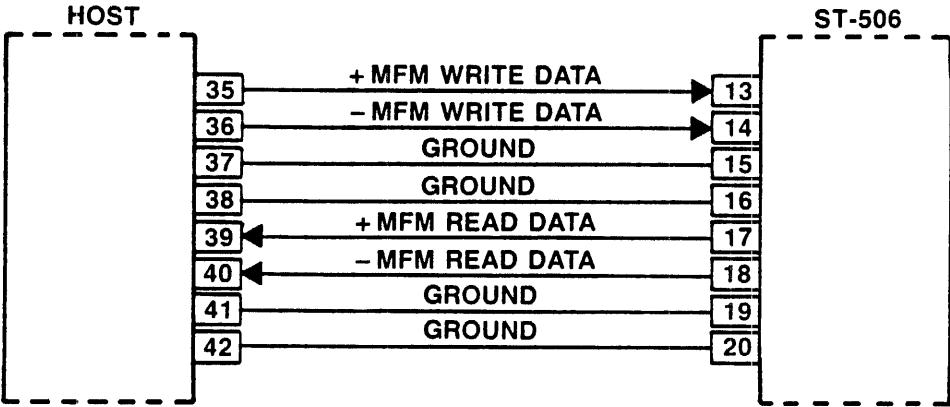
Figure 8-2 shows the pin/signal assignments for control signal interface between the DM01 controller and an ST-506 disk drive. The control signal interface has a maximum cable length of 20 feet. Figure 8-3 shows the pin/signal assignments for modified data signal interface between the DM01 controller and an ST-506 disk drive. The data signal interface also has a maximum cable length of 20 feet.



\* DRIVES WITH MORE THAN 8 HEADS USE HEAD SELECT 2<sup>3</sup>

DM0102-0920

Figure 8-2. Control Pin/Signal Assignments at ST-506 Disk Drive Interface



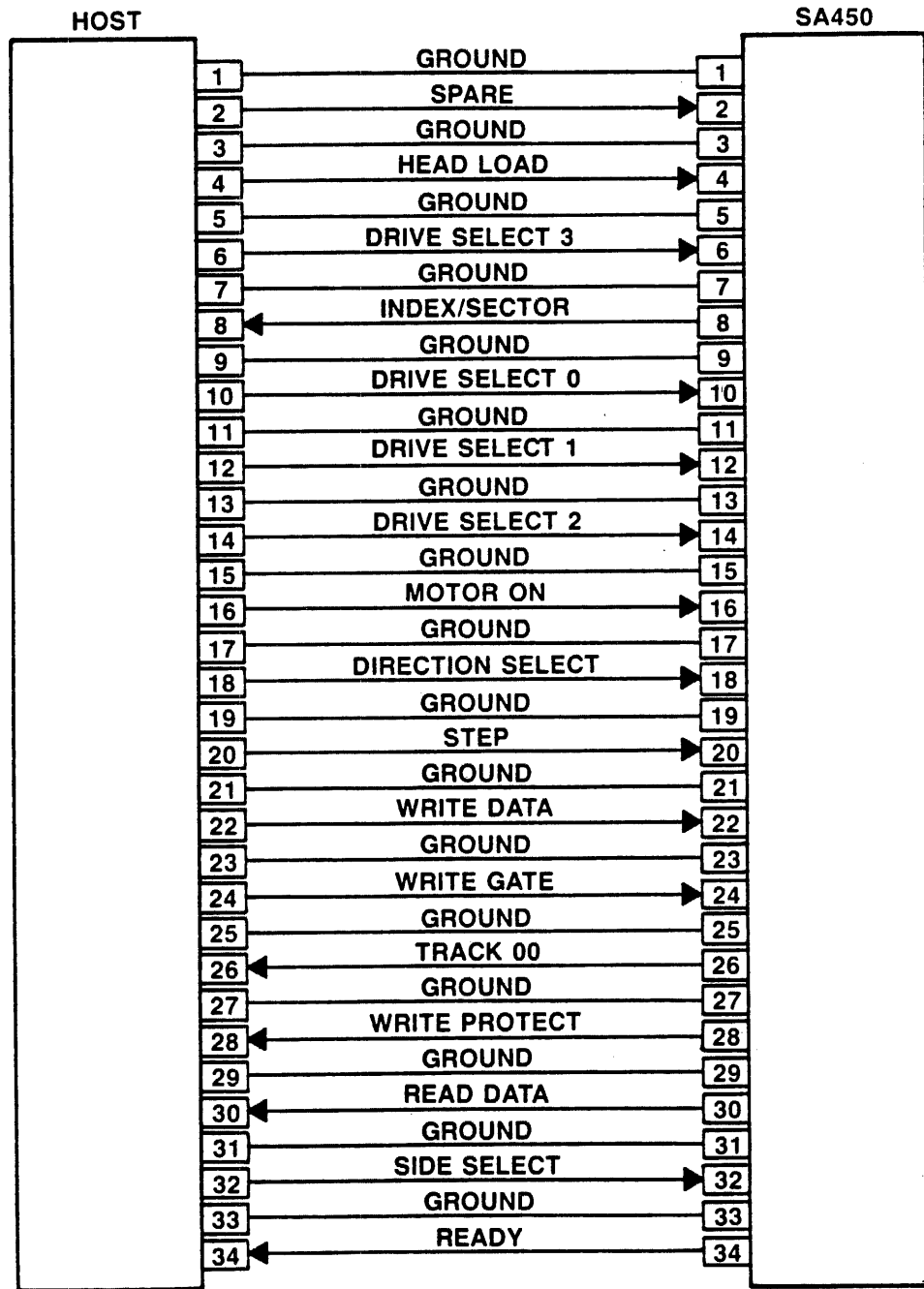
NOTE: HOST PINS 43-50  
ARE USED FOR SECOND  
ST-506 DRIVE

DM0102-0921

Figure 8-3. Data Pin/Signal Assignments at ST-506  
Disk Drive Interface

## 8.4 DM01 SA450 Floppy Drive Interface

The DM01 Controller interfaces with each SA450 floppy drive via a 34-pin cable from a 34-pin male connector at reference designator J3 on the DM01 PCBA. Figure 8-4 shows the pin/signal assignments for SA450 signal interface between the DM01 Controller and an SA450 floppy disk drive. Note that the DM01 uses pin 4 on its SA450 interface for the HEAD LOAD signal. The SA450 signal interface has a maximum cable length of 10 feet.



DM0102-0922

Figure 8-4. Pin/Signal Assignments at SA450 Floppy Drive Interface

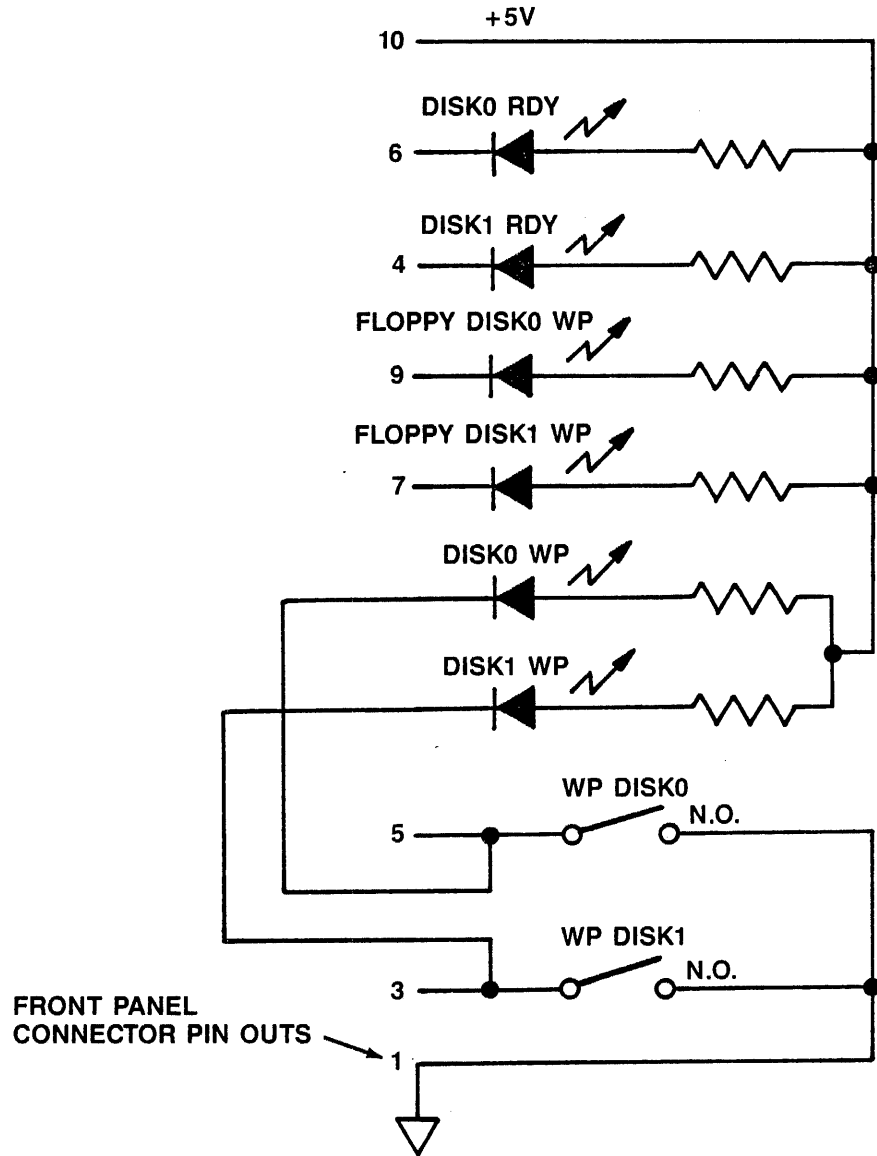
## 8.5 Front Panel Interface

The DM01 provides an interface that allows a remote control and status panel to be connected to the controller. The interface allows write-protect switches for each ST-506 drive to be connected, and it provides drivers for ready and write-protected status LEDs, and allows for write protect status for two floppy drives. Note that the DM01 updates only the floppy write-protect LEDs on a write operation to the floppy drives.

The interface is implemented by using a four-wall, right-angle header (3M part number 3591-5002) designated J4. The header has 10 pins. The function of each pin is described in Table 8-2. Figure 8-5 shows the pin-outs and a sample user interface.

Table 8-1. Control and Status Interface Pin Function Description

Pin	Function	Description
1	Ground	Controller Logic Ground
2	Not Connected	
3	Hard Disk 1 Write Protect Input	Ground this line to write protect hard disk 1
4	Hard Disk 1 Ready Status	This line sinks 24 mA when hard disk 1 is ready
5	Hard Disk 0 Write Protect Input	Ground this line to write protect hard disk 0
6	Hard Disk 0 Ready Status	This line sinks 24 mA when hard disk 0 is ready
7	Floppy Disk 1 Write Protect Status	This line sinks 24 mA when floppy disk 1 is write protected
8	Not connected	
9	Floppy Disk 0 Write Protect Status	This line sinks 24 mA when floppy disk 0 is write protected
10	+5 Vdc	This line provides 5 Vdc. This line is not current protected.



DM0102-0622

Figure 8-5. Status and Control Interface



---

## **A.1 Overview**

---

The following discussion presents the algorithm for assignment of floating addresses and vectors for all DEC operating systems. Bus addresses are discussed in subsection 3.3.2.

---

## **A.2 Determining the CSR Address for Use With Autoconfigure**

---

The term Autoconfigure refers to a software utility that is run when the computer is bootstrapped. This utility finds and identifies I/O devices in the I/O page of system memory.

Some devices (like the DM11) have fixed addresses reserved for them. Autoconfigure detects their presence by simply testing their standard address for a response. Specifically, the control/status register (CSR) address, which is usually the first register of the block, is tested.

Addresses for those devices not assigned fixed numbers are selected from the floating CSR address space (760010 - 763776) of the UNIBUS input/output (I/O) page. This means that the presence or absence of floating devices will affect the assignment of addresses to other floating-address devices. Similarly, many devices have floating interrupt vector addresses. According to the DEC standard, vectors must be assigned in a specific sequence and the presence of one type of device will affect the correct assignment of vectors for other devices.

The CSR address for a floating-address device is selected according to the algorithm used during autoconfigure. The algorithm is used in conjunction with a Device Table, Table A-1.

Essentially, Autoconfigure checks each valid CSR address in the floating CSR address space for the presence of a device. Autoconfigure expects any devices installed in that space to be in the order specified by the Device Table. Also, the utility expects an eight-byte block to be reserved for each device that is not installed in the system. Each empty block tells Autoconfigure to look at the next valid address for the next device on the list.

When a device is detected, a block of addresses is reserved for the device according to the number of registers it employs. The utility then looks at the next CSR for that device type. If there is a device there, it is assumed to be of the same type as the one before it and a block is reserved for that device. If there is no response at the next address, that space is reserved to indicate that there are no more devices of that type. Then the utility checks the CSR address (at the appropriate boundary) for the next device in the table.

Table A-1. SYSGEN Device Table

Rank	Device	Number of Registers	Octal Modulus	Rank	Device	Number of Registers	Octal Modulus
1	DJ11	4	10	17	Reserved	4	10
2	DH11	8	20	18	RX11 <sup>2</sup>	4	10
3	DQ11	4	10	18	RX211 <sup>2</sup>	4	10
4	DU11,DUV11	4	10	18	RXV11 <sup>2</sup>	4	10
5	DUP11	4	10	18	RXV21 <sup>2</sup>	4	10
6	LK11A	4	10	19	DR11-W	4	10
7	DMC11	4	10	20	DR11-B <sup>3</sup>	4	10
7	DMR11	4	10	21	DMP11	4	10
8	DZ11 <sup>1</sup>	4	10	22	DPV11	4	10
8	DZV11	4	10	23	ISB11	4	10
8	DZS11	4	10	24	DMV11	8	20
8	DZ32	4	10	25	DEUNA <sup>2</sup>	4	10
9	KMC11	4	10	26	UDA50 <sup>2</sup>	2	4
10	LPP11	4	10	27	DMF32	16	40
11	VMV21	4	10	28	KMS11	6	20
12	VMV31	8	20	29	VS100	8	20
13	DWR70	4	10	30	TU81	2	4
14	RL11 <sup>2</sup>	4	10	31	KMV11	8	20
14	RLV11 <sup>2</sup>	4	10	32	DHV11	8	20
15	LPA11-K <sup>2</sup>	8	20	33	DMZ32	16	40
16	KW11-C	4	10	34	CP132	16	40

<sup>1</sup> DZ11-E and DZ11-F are treated as two DZ11s.  
<sup>2</sup> The first device of this type has a fixed address. Any extra devices have a floating address.  
<sup>3</sup> The first two devices of this type have a fixed address. Any extra devices have a floating address.

In summary, there are four rules that pertain to the assignment of device addresses in floating address space:

1. Devices with floating addresses must be attached in the order in which they are listed in the Device Table, Table A-1.

- The CSR address for a given device type is assigned on word boundaries according to the number of UNIBUS-accessible registers that the device has. The following table relates the number of device registers to possible word boundaries.

Device Registers	Possible Boundaries
1	Any Word
2	XXXXX0, XXXXX4
3,4	XXXXX0
5,6,7,8	XXXXX0, XXXX20, XXXX40, XXXX60
9 thru 16	XXXXX0, XXXX40

The Autoconfigure utility inspects for a given device type only at one of the possible boundaries for that device. That is, the utility does not look for a DMF32 (16 registers) at an address that ends in 20.

- An 8-byte gap must follow the register block of any installed device to indicate that there are no more of that type of device. This gap must start on the proper CSR address boundary for that type of device.
- An 8-byte gap must be reserved in floating address space for each device type that is not installed in the current system. The gap must start on the proper word boundary for the type of device the gap represents. That is, a single DJ11 installed at 760010 would be followed by a gap starting at 760020 to show a change of device types. A gap to show that there are none of the next device on the list, a DH11, would begin at 760040, the next legal boundary for a DH11-type device.

---

### A.3 Determining the Vector Address for Use With Autoconfigure

---

There is a floating vector address convention that is used for communications and other devices which interface with the Unibus. These vector addresses are assigned in order starting at 300 and proceeding upwards to 777. Table A-2 shows the assignment sequence. For a given system configuration, the device with the highest floating vector rank would be assigned to vector address 300. Additional devices of the same type would be assigned subsequent vector addresses according to the number of vectors required per device, and according to the starting boundary assigned to that device type.

Table A-2. Priority Ranking for Floating Vector Addresses (starting at 300<sub>8</sub> and proceeding upwards)

Rank	Device	Number of Vectors	Octal Modulus
1	DC11	2	10
1	TU58	2	10
2	KL11 <sup>1</sup>	2	10
2	DL11-A <sup>1</sup>	2	10
2	DL11-B <sup>1</sup>	2	10
2	DLV11-J <sup>1</sup>	8	40
2	DLV11,DLV11-F <sup>1</sup>	2	10
3	DP11	2	10
4	DM11-A	2	10
5	DN11	1	4
6	DM11-BB/BA	1	4
7	DH11 modem control	1	4
8	DR11-A, DRV11-B	2	10
9	DR11-C, DRV11	2	10
10	PA611 (reader + punch)	4	20
11	LPD11	2	10
12	DT07	2	10
13	DX11	2	10
14	DL11-C to DLV11-F	2	10
15	DJ11	2	10
16	DH11	2	10
17	VT40	4	20
17	VSV11	4	10
18	LPS11	6	40
19	DQ11	2	10
20	KW11-W, KWV11	2	10
21	DU11, DUV11	2	10
22	DUP11	2	10
23	DV11 + modem control	3	20
24	LK11-A	2	10
25	DWUN	2	10
26	DMC11	2	10
26	DMR11	2	10
27	DZ11/DZS11/DZV11	2	10
27	DZ32	2	10
28	KMC11	2	10
29	LPP11	2	10
30	VMV21	2	10
31	VMV31	2	10
32	VTV01	2	10
33	DWR70	2	10
34	RL11/RLV11 <sup>2</sup>	1	4
35	TS11 <sup>2</sup> , TU80 <sup>2</sup>	1	4

(continued on next page)

Table A-2. Priority Ranking for Floating Vector Addresses (starting at 300<sub>8</sub> and proceeding upwards) (continued)

Rank	Device	Number of Vectors	Octal Modulus
36	LPA11-K	2	10
37	IP11/IP300 <sup>2</sup>	1	4
38	KW11-C	2	10
39	RX11 <sup>2</sup>	1	4
39	RX211 <sup>2</sup>	1	4
39	RXV11 <sup>2</sup>	1	4
39	RXV21 <sup>2</sup>	1	4
40	DR11-W	1	4
41	DR11-B <sup>2</sup>	1	4
42	DMP11	2	10
43	DPV11	2	10
44	ML11 <sup>3</sup>	1	4
45	ISB11	2	10
46	DMV11	2	10
47	DEUNA <sup>2</sup>	1	4
48	UDA50 <sup>2</sup>	1	4
49	DMF32	8	40
50	KMS11	3	20
51	PCL11-B	2	10
52	VS100	1	4
53	Reserved	1	4
54	KMV11	2	10
55	Reserved	2	10
56	IEX	2	10
57	DHV11	2	10
58	DMZ32	6	20
59	CP132	6	20

<sup>1</sup> A KL11 or DL11 used as a console, has a fixed vector.

<sup>2</sup> The first device of this type has a fixed vector. Any extra devices have a floating vector.

<sup>3</sup> ML11 is a Massbus device which can connect to a UNIBUS via a bus adapter.

Vector addresses are assigned on the boundaries indicated in the modulus column of Table A-2. That is, if the modulus is 10, then the first vector address for that device must end with zero (XX0). If the modulus is 4, then the first vector address can end with zero or 4 (XX0, XX4).

Vector addresses always fall on modulo 4 boundaries (XX0, XX4). That is, a vector address never ends in any number but four or zero. Consequently, if a device has two vectors and the first must start on a modulo 10 boundary, then, using 350 as a starting point, the vectors will be 350 and 354.

---

## **A.4 A System Configuration Example**

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Table A-3 contains an example of a system configuration that includes devices with fixed addresses and vectors, and floating addresses and/or vectors.

Table A-4 shows how the device addresses for the floating address devices in Table A-3 were computed, including gaps.

Table A-3. CSR and Vector Address Example

<b>Controller</b>	<b>Vector</b>	<b>CSR</b>
1 UDA50	154	772150
1 DZ11	300	760100
1 UDA50	310	760354
2 DHV11	320	760500

Table A-4. Floating CSR Address Assignment Example

Installed	Device		Octal Address
	DJ11	Gap	760010
	DH11	Gap	760020
	DQ11	Gap	760030
	DU11	Gap	760040
	DUP11	Gap	760050
	LK11A	Gap	760060
	DMC11	Gap	760070
---->	DZ11		760100
		Gap	760110
	KMC11	Gap	760120
	LPP11	Gap	760130
	VMV21	Gap	760140
	VMV31	Gap	760150
	DWR70	Gap	760170
	RL11	Gap	760200
	LPA11-K	Gap	760220
	KW11-C	Gap	760230
	Reserved	Gap	760240
	RX11	Gap	760250
	DR11-W	Gap	760260
	DR11-B	Gap	760270
	DMP11	Gap	760300
	DPV11	Gap	760310
	ISB11	Gap	760320
	DMV11	Gap	760340
	DEUNA	Gap	760350
--->	UDA50 (DM01)		772150 <sup>1</sup>
--->	UDA50 (DM01)		760354
		Gap	760360
	DMF32	Gap	760400
	KMS11	Gap	760420
	VS100	Gap	761440
	TU81	Gap	761450
	KMV11	Gap	761460
--->	DHV11		761500
--->	DHV11		761520
		Gap	761530
	DMZ32	Gap	761540
	CP132	Gap	761600

<sup>1</sup>Fixed address





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**Appendix B**  
**PROM Removal and Replacement**

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**B.1 Overview**

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This appendix provides instructions for replacing the DM01's firmware PROM.

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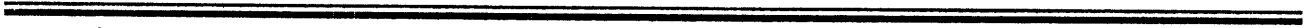
**B.2 Exchanging PROMs**

---

The DM01 firmware PROM is located in the socket at U46. Pry the existing PROM from its socket using an IC puller or an equivalent tool.

The DM01 PROM is identified by the part numbers on top of the PROMs. Place the DM01 PROM in socket U46. Make certain that the PROM is firmly seated and that no pins are bent or misaligned. (If the two rows of PROM pins are too far apart to fit in the socket, grasp the PROM at its ends using your thumb and forefinger and bend one of the pin rows inward by pressing it against a table top or other flat surface.)

PROM Number	PCBA Location
E52	U46



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**Appendix C**  
**DISK DRIVE CONFIGURATION**

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**C.1 Overview**

---

This appendix contains the configuration parameters for ST-506 and SA450 disk drives, presented in Tables C-1 and C-2. Refer to the drive manufacturer's manual for specific instructions on setting the drive for the required options.

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**C.2 Parameter Values**

---

The drive configuration parameters listed in this table are for use with the NOVRAM loading, editing, and displaying options of DM01's firmware-resident diagnostics. They relate to the physical geometry of the disk drives; options such as logical splits are left to you.

The configuration table lists each parameter as it is displayed by the diagnostic, as well as each drive certified for use with the DM01. Parameter values in this table are based on one spare sector per track with no logical splits. Values are listed and entered in decimal.

To use the table, locate the name of your drive along the top of the table. Then read down the column beneath the drive name for the parameter values.

If you are looking for a specific parameter value, find the parameter you need in the "parameter" column. Then find your drive in the "drive name" row. The parameter value is listed at the point where the row and the column intersect.



PARAMETER	NEC	PANASONIC	TANDON	TEAC	TEAC
	FD1055	JU-465-5	TM65-4	FD-55F	FD-55FV
Type Code	2	4	4	2	2
Number of Units of this Type	1	1	1	1	1
Step Rate Code	0	0	0	0	0

Table C-2. SA450 DRIVE CONFIGURATION PARAMETER VALUES for DM01





# ENGINEERING CHANGE ORDER

ECO NO. 05511      ISSUE DATE 4/14/89  
 ECR NO. None      SHEET 1 OF 1

REQUESTER  
L. Bridges

DATE  
4-14-89

APPROVAL *[Signature]* DATE 4/12/89

MODEL/EQUIPMENT AFFECTED  
DM01

**EFFECTIVITY**  
 ALL UNITS IN HOUSE  
 ALL UNITS IN PROCESS  
 NEXT BUY/BUILD  
 RECORD

**AFFECTED**  
 MANUALS  
 FIRMWARE  
 SOFTWARE  
 DIAGNOSTICS  
 PRODUCTION TOOLING  
 CONFIGURATOR

**MATERIAL DISPOSITION**  
 1. USE AS IS  
 2. REWORK  
 3. PHASE-IN  
 4. NOT APPLICABLE  
 5. SCRAP

**TYPE**  
 CORRECTIVE  
 MAINTAINABILITY  
 RECORD CHANGE  
 DEVIATION

**PRIORITY**  
 1 CRITICAL  
 2 URGENT  
 3 ROUTINE

EFFECTIVE DATE  
 \_\_\_/\_\_\_/\_\_\_

**REASON FOR CHANGE**  
 Rev B corrects the Control Pin/Signal Assignments at ST-506 Disk Drive Interface, and one editorial error.

**WHERE USED: LIST BELOW ALL DOCUMENTS, PARTS AND ASSEMBLIES THAT ARE AFFECTED BY THIS CHANGE ORDER.**

WHERE USED: LIST BELOW ALL DOCUMENTS, PARTS AND ASSEMBLIES THAT ARE AFFECTED BY THIS CHANGE ORDER.				MATERIAL DISPOSITION CODE							
DOC/PART/ASSY NUMBER	CUR REV	NEW REV	TITLE/DESCRIPTION	NEXT ORDER	ON ORDER	RECEIVING/INSPECTION	STOCKROOM	WORK IN PROGRESS	FINISHED GOODS		
DM0151002-00	A	B	DM01 Disk Controller Tech. Man.	3	1	1	1	1	1		

**Instructions:**

Rework DM0151002-00 Rev A to Rev B by replacing title page (i), 5-1, and 8-4. Change pages reproduced in house.

**COMMENTS**

*E*

**FIELD CHANGE NOTICE**  
 YES  
 NO

ENGR SERVICES    ENGINEERING    MANUFACTURING    PURCHASING    QUALITY    CUST SUPPORT    MARKETING

*[Signatures]*



# ENGINEERING CHANGE ORDER

NO. 4971 DATE 02 Aug

PAGE 1 OF 1

**EQUIPMENT AFFECTED:**

DM01

- CORRECTIVE  
 MAINTAINABILITY  
 RECORD CHANGE  
 DEVIATION  
 RELEASE

**EFFECTIVITY:**

Initial Release

- YES NO**  
  FCO REQUIRED  
  SOFTWARE AFFECTED  
  PUBLICATIONS AFFECTED

**REASON FOR CHANGE:**

Initial Release

- DISPOSITION:**  
 1. USE AS IS  
 2. REWORK  
 3. CHANGE PROMS  
 4. NOT APPLICABLE  
 5. INCORPORATE  
 6. SCRAP  
 7. SEE NOTE ON PAGE \_\_\_\_

DOCUMENT NUMBER	REV		TITLE/DESCRIPTION	NEXT ORDER	ON ORDER AT VENDOR	RECEIVING & INSPECTION	IN STOCK	IN PROCESS	SPARES
	FROM	TO							
DM0151002-00	-	A	DM01 Disk Controller						
			Technical Manual	5	5	5	5	5	5
SKLDM01XX-00	2	3	DM01 Shipping Kit List	5	4	4	4	4	4

**Instructions:**

Use DM0151002-00 Rev A with DM01 Config. Spec. DM0150001 Rev B and above.  
 Print and distribute DM0151002-00 Rev A.  
 Reproduced in house.

**DISPOSITION NOTE:**

ORIGINATOR L. Bridges	DESIGN SERVICES <i>[Signature]</i>	PROJECT ENGINEER <i>[Signature]</i>	MANUFACTURING <i>[Signature]</i>	NO. 4971
QUALITY CONTROL <i>[Signature]</i>	FIELD SERVICE <i>[Signature]</i>	TEST ENGINEERING <i>[Signature]</i>	CERTIFICATION	



**EMULEX CORPORATION  
DOCUMENTATION SHIPPING KIT LIST**

**DM01 DISK CONTROLLER**

**NOTE**

This shipping kit list, SKLDM01XX-00. Rev 003 and above, is for use with DM01 Disk Controller DM0150001-00 Rev B and above ONLY. The label is on the PROM at location E52. DM01 Disk Controllers with PROMs labeled Rev B and below must use SKLDM01XX-00 Rev 002.

The following documents make up the documentation package for the DM01 Disk Controller:

Qty	Part Number	Revision Level	Description
1	DM0151002-00	A	DM01 Technical Manual



## Reader's Comments

Your comments and suggestions will help us in our continuous effort to improve the quality and usefulness of our publication.

Manual Part Number \_\_\_\_\_ Rev. \_\_\_\_\_

What is your general reaction to this manual? In your judgment is it complete, accurate, well organized, well written, etc.? Is it easy to use? \_\_\_\_\_

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What features are most useful? \_\_\_\_\_

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What faults or errors have you found in the manual? \_\_\_\_\_

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Does this manual satisfy the need you think it was intended to satisfy? \_\_\_\_\_

Does it satisfy *your* needs? \_\_\_\_\_ Why? \_\_\_\_\_

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Please send me the current copy of the *Controller Handbook*, which contains the information on the remainder of EMULEX's controller products.

Name \_\_\_\_\_

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