FLOATING POINT SYSTEMS, INC.

AP-190L-6

PART NUMBER	DESCRIPTION	PRICES
AP-190L-6	**ARRAY PROCESSOR SYSTEM** Includes: • AP-120B Floating Point Array Processor. • 32K words of 38-bit MOS semiconductor RAM Memory. 167ns interleaved effective cycle time. • 2.5K words of 38-bit bipolar ROM Table Memory. • 2K words of 38-bit bipolar RAM Table Memory. • 2K words of 64-bit bipolar RAM, Writable Control Store. • AP Software Development Package. • Algorithm Library. • 19" Rack Mount Cabinet. • Hardware Interface to Computer Mainframe. • Software Interface to Mainframe Operating System. • Installation	$106,465.00
AP-190-DMF32	**MAIN DATA MEMORY.** 32,768 words of 38-bit MOS semiconductor RAM Memory. 167ns interleaved effective cycle time. (System maximum is one megaword.)	26,470.00
AP-190-DMF64	**MAIN DATA MEMORY.** 65,536 words of 38-bit MOS semiconductor RAM Memory. 167ns interleaved effective cycle time. (System maximum is one megaword.)	43,920.00
AP-190-PAGE	Page Select Option for Main Data Memory expansion beyond 64K.	2,100.00
AP-190-TMR	**RAM TABLE MEMORY.** 1024 words of 38-bit bipolar RAM. (System maximum is 64K words.)	1,750.00
AP-190-TM	**TABLE MEMORY.** 512 words of 38-bit bipolar ROM for additional constant storage. (System maximum is 64K words.)	645.00
AP-190-PS	**PROGRAM SOURCE MEMORY.** 1K words of 64-bit bipolar RAM, used to hold microprograms. (System maximum is 4K words.)	6,380.00
AP-190-PAR	**PARITY OPTION.** Implementation of 3 parity bits for checking data coming from and going to Main Data Memory. Basic Option and first 32K Main Data Memory.	3,100.00
	For each additional 32K Main Data Memory module, add	700.00

FPS-7344 June 1977. Prices and Specifications subject to change without notice.

AP120B 45
FPP as add on $40-50 K