

# OPERATING MANUAL CHANGES

MANUAL IDENTIFICATION	
Model Number:	64193A
Manual Title:	6805P Emulator/Analyzer
Part Number:	64193-90903
Date Printed:	April 1983

This supplement contains important information for correcting manual errors and for adapting the manual to instruments containing improvements made after the printing of the manual.

To use this supplement:

Make all ERRATA corrections.

Make all appropriate software or hardware related changes indicated in the table below.

Software		Hardware	Make Manual Changes
Model - Rev.	Date	Serial Prefix or Number	

## ERRATA

Page 1-2, Installing Emulation Pod and Emulation Control Board  
Add the following to the first paragraph:

For emulation of a 6805R/U series microprocessor (6805R2, 6805R3, 6805U2, 6805U3) the Model 64192A emulator pod and Model 64191A emulation control board are required.

Replace paragraph three with:

The emulation control cable is covered by a black shield that must be connected to the Model 64000 chassis ground. Refer to the Model 64192A (6805R/U) or Model 64193A (6805P) Service Manual for the installation procedure.

## NOTE

Manual change supplements are revised as often as necessary to keep manuals as current and accurate as possible. Hewlett-Packard recommends that you periodically request the latest edition of this supplement. When requesting copies quote the manual identification information from your supplement, or the model number and print date from the title page of the manual.

Date: 1 August 1983

Page: 1 of 4

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### ERRATA (Cont'd)

Page 1-3, Installing The Analysis Board,  
Replace first paragraph with:

Either the Model 64300A or the Model 64301A Internal Analysis board can be used with the 6805 emulators.

Page 2-7, Emulator Operating Modes, last paragraph,  
Change to read:

Host processor "reads" and "writes" to emulation memory are in the nonreal-time mode for the 6805 emulators.

Page 3-1, Processor Architecture,  
At end of first sentence add:

(12-bit address bus 6805R/U).

After "11-bit program counter (PC)" Add:

(program counter 12 bits for 6805R/U)

After "11-bit stack pointer register (SP)" Add:

(stack pointer 8 bits for 6805R/U)

Last paragraph, last sentence,

Delete: "for the 6805P Series."

Page 4-9, Microprocessor Selection,  
After first line, Add:

Or,

Microprocessor to be emulated: M6805R2 (M6805R3) (M6805U2)  
(M6805U3)

First paragraph, last line,

Change to read: Table 4-1 describes the maps for 6805P and 6805R/U processors.

Date: 1 August 1983

Page: 2 of 4



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### ERRATA (Cont'd)

Page 4-9, Table 4-1,

After the current table, add the following:

Memory Map	M6805R2/U2	M6805R3/U3
0-3F	Internal	Emul/RAM
40-7F	Emul/RAM	Emul/RAM
80-FF	Emul/ROM	Emul/ROM
100-7BF	Guarded	Emul/ROM
7C0-FFF	Emul/ROM	Emul/ROM

Page 4-9, NOTE

Immediately after NOTE add:

Memory locations 10H-3FH cannot be displayed for the M6805R2/U2. Data will always be read as OFFH and displayed as "\*".

Add to the list of explanations after Emul:

Internal - designates address space that is dedicated to I/O ports and control registers.

Page 4-10, Timer Selection,

Change first paragraph to read:

The M6805R3, M6805U3, and M6805P3 microprocessors allow the user to select the timer mode. For mask option use the soft key: mask\_opt.

Change second paragraph to read:

Timer source selection for the M6805R2/U2, M6805P2/P4, and mask option M6805R3/U3 and M6805P3 follows. For internal\_gated option use the soft key: gated.

Date: 1 August 1983

Page: 3 of 4



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### ERRATA (Cont'd)

Page 4-10, (Cont'd)

Change third paragraph to read:

For the software option M6805R3/U3 and M6805P3:

Appendix B,

These messages also apply to the M6805R/U series.

Date: 1 August 1983

Page: 4 of 4



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