Classic/PCI Expandable Desktop
Jumpers & Connectors

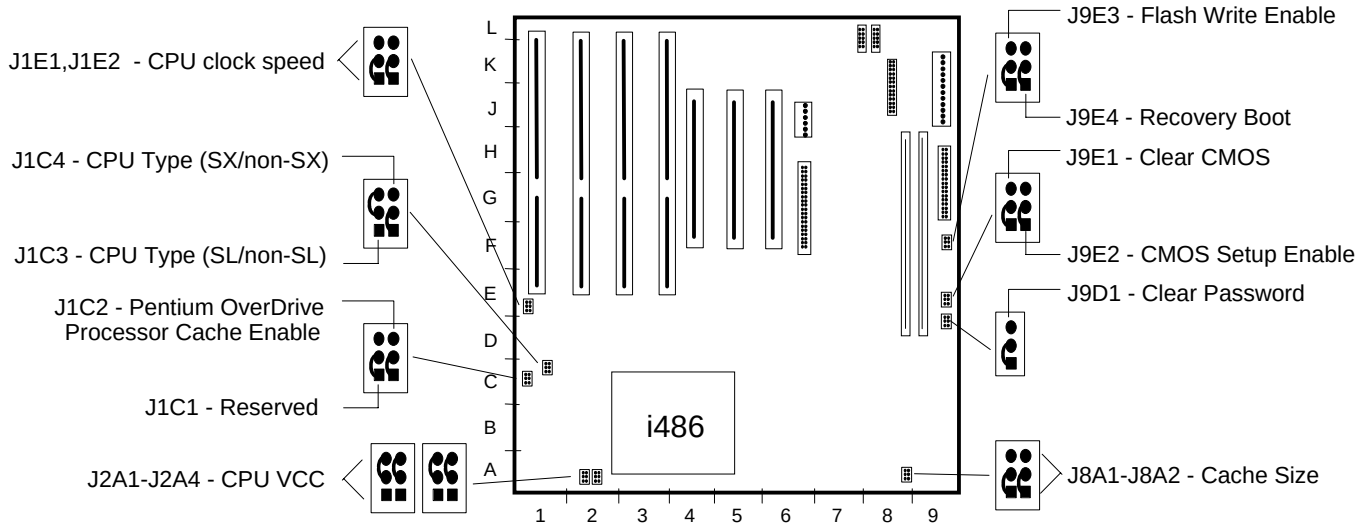

Figure B-1. Jumper locations and default settings (■ denotes Pin 1)

CPU CLOCK SPEED – J1E1,J1E2

There are two jumpers to select 25/33 MHz processor clock. Both jumpers must have the same configuration for the CPU clock speed to be properly set. The default setting is determined by the processor shipped on the baseboard. Baseboards shipped without processors will be configured for a 33 MHz clock (1-2).

1-2 = 33 MHz
2-3 = 25 MHz

CPU TYPE (SX/NON-SX) – J1C4

This jumper is set to position 2-3 for i486 SX processors and set to 1-2 for all other Intel processors. The default setting is determined by the processor shipped on the baseboard. Baseboards shipped without processors will be configured for non SX processors (1-2).

1-2 = non SX processors
2-3 = i486 SX processors

CPU TYPE (SL/NON-SL) – J1C3

This jumper is set to position 1-2 for non-SL enhanced processors and set to 2-3 for SL-enhanced processors. All baseboards and systems shipped with processors installed will have SL-enhanced processors.

1-2 = non SL-enhanced processors
2-3 = SL-enhanced processors (default)

PENTIUM OVERDRIVE PROCESSOR CACHE ENABLE – J1C2

This jumper is set to position 1-2 normally. When installing a Pentium OverDrive processor this jumper must be changed to 2-3 to properly set up the L2 cache.

1-2 = non Pentium OverDrive processors (default)
2-3 = Pentium OverDrive cache enabled

RESERVED – J1C1

This jumper is reserved and should not be altered.

CPU VCC (5V/3.3V) – J2A1,J2A2,J2A3,J2A4

There are four jumpers that need to be moved to allow the use of IntelDX4 microprocessors which operate at 3.3 volts. The jumpers select the Vcc source for the CPU. The jumpers must all be on the 3.3 volt position for a 3.3V microprocessor or in the 5 volt position for all other CPU types. The default is 2-3 for 5 volt operation.

1-2 = 3.3 volt
2-3 = 5 volt (default)

WARNING: Do NOT move these jumpers while the power is on! The CPU could be damaged.

FLASH WRITE ENABLE (BW) – J9E3

Allows reprogramming of Flash EPROM to be disabled

1-2 = Flash Write Enabled (default)
2-3 = Flash Write Protected

RECOVERY BOOT (RV) – J9E4

Allows recovery if the system FLASH update process results in corrupted EPROM

1-2 = Normal Boot (default)
2-3 = Recovery Boot

CLEAR CMOS (CM) – J9E1

Allows CMOS to be reset to default settings.

1-2 = Normal (default)
2-3 = Clear CMOS to default settings

SETUP ENABLE (SE) – J9E2

Allows access to CMOS Setup Utility to be disabled.

1-2 = Access to Setup allowed (default)
2-3 = Access to Setup prevented

CLEAR PASSWORD (PW) – J9D1

Allows system password to be cleared.

1-2 = Password Enabled (default)
2-3 = Clear Password

CACHE SIZE – J8A1,J8A2

Two jumpers to select cache options of 128 KB/256 KB. Both jumpers must have the same configuration for the cache to be properly set. If secondary cache is not installed, this jumper setting has no effect.

1-2 = 256 KB (default)
2-3 = 128 KB

Connectors

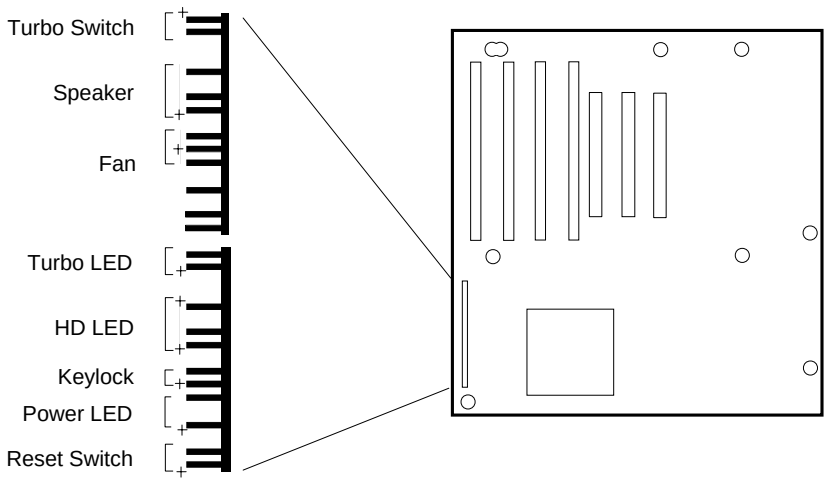

Figure C-1. Front Panel header connectors

TURBO SWITCH

Pin	*Signal Name*
1	TURBO
2	Ground
3	No Connect

SPEAKER CONNECTOR

Pin	*Signal Name*
1	SPKR_DAT
2	Key
3	No Connect
4	+5V Vcc

FAN CONNECTOR

Pin	*Signal Name*
1	Ground
2	+12V
3	Ground

TURBO LED

Pin	*Signal Name*
1	PULL_UP_330
2	LED_TURBO-

HARD DRIVE LED

Pin	*Signal Name*
1	PULL_UP_330
2	HD ACTIVE-
3	Key
4	PULL_UP_330

KEYLOCK/POWER LED

Pin	*Signal Name*
1	LED_PWR
2	Key
3	Ground
4	KEY LOCK
5	Ground

RESET SWITCH

Pin	*Signal Name*
1	RESET
2	Ground

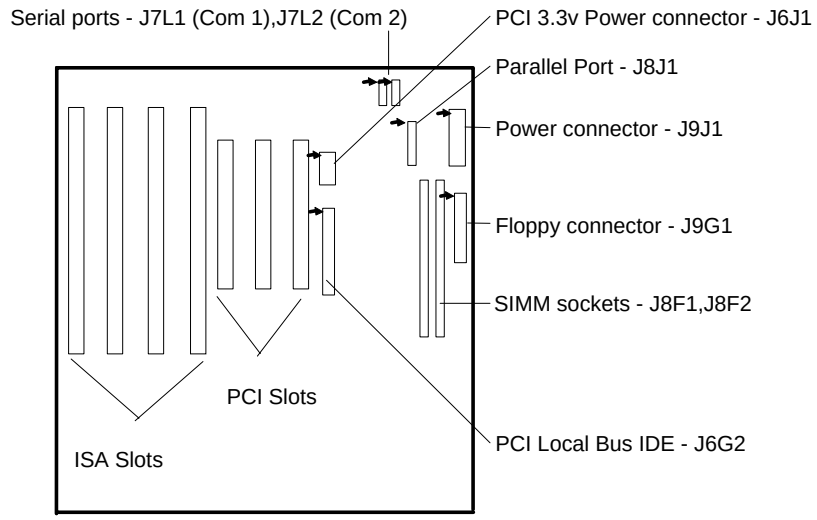

Figure C-2. Classic/PCI baseboard connectors (→ indicates Pin 1 location)

SERIAL PORTS - J7L1,J7L2

PIn	*Signal Name*
1	DCD
2	DSR
3	Serial In - (SIN)
4	RTS
5	Serial Out - (SOUT)
6	CTS
7	DTR
8	RI
9	GND
10	N.C.

PCI (3.3V) POWER - J6J1

Pin	*Name*	*Function*
1	GND	Ground
2	GND	Ground
3	GND	Ground
4	+3.3 V	+ 3.3 volts
5	+3.3V	+ 3.3 volts
6	+3.3 V	+ 3.3 volts

PARALLEL PORT - J8J1

Signal Name	*Pin*	*Pin*	*Signal Name*
STROBE-	1	2	AUTO
Data Bit 0	3	4	ERROR-
Data Bit 1	5	6	INIT-
Data Bit 2	7	8	SLCT IN-
Data Bit 3	9	10	Ground
Data Bit 4	11	12	Ground
Data Bit 5	13	14	Ground
Data Bit 6	15	16	Ground
Data Bit 7	17	18	Ground
ACK-	19	20	Ground
BUSY	21	22	Ground
PE (Paper End)	23	24	Ground
SLCT	25	26	N.C.

POWER CONNECTOR - J9J1

Pi	*Name*	*Function*
1	PWRGD	Power Good
2	+5 V	+ 5 volts Vcc
3	+12 V	+ 12 volts
4	-12 V	- 12 volts
5	GND	Ground
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	-5 V	-5 volts
10	+5 V	+ 5 volts Vcc
11	+5 V	+ 5 volts Vcc
12	+5 V	+ 5 volts Vcc

FLOPPY CONNECTOR - J9G1

Signal Name	*Pin*	*Pin*	*Signal Name*
Ground	1	2	FDHDIN
Ground	3	4	Reserved
Key	5	6	FDEDIN
Ground	7	8	Index-
Ground	9	10	Motor Enable A-
Ground	11	12	Drive Select B-
Ground	13	14	Drive Select A-
Ground	15	16	Motor Enable B-
Ground	17	18	DIR-
Ground	19	20	STEP-
Ground	21	22	Write Data-
Ground	23	24	Write Gate-
Ground	25	26	Track 00-
Ground	27	28	Write Protect-
Ground	29	30	Read Data-
Ground	31	32	Side 1 Select-
Ground	33	34	Diskette

IDE CONNECTOR - J6G2

Signal Name	*Pin*	*Pin*	*Signal Name*
Reset IDE	1	2	Ground
Host Data 7	3	4	Host Data 8
Host Data 6	5	6	Host Data 9
Host Data 5	7	8	Host Data 10
Host Data 4	9	10	Host Data 11
Host Data 3	11	12	Host Data 12
Host Data 2	13	14	Host Data 13
Host Data 1	15	16	Host Data 14
Host Data 0	17	18	Host Data 15
Ground	19	20	Key
N.C.	21	22	Ground
I/O Write-	23	24	Ground
I/O Read-	25	26	Ground
IOCHRDY	27	28	BALE
N.C.	29	30	Ground
IRQ14 (J6G2)	31		
IRQ15 (J6G1)	31	32	IOCS16-
Addr 1	33	34	N.C.
Addr 0	35	36	Addr 2
Chip Sel0- (cs1xx)	37	38	Chip Sel1-
Activity	39	40	Ground

AT STYLE KEYBOARD PORT - J8L1

Pin	*Signal Name*
1	Clock
2	Data
3	No Connect
4	Ground
5	Vcc (fused)

ISA CONNECTORS - J1G1,J2G1,J2G2, J3G1

Signal Name	*Pin*	*Pin*	*Signal Name*
GND	B1	A1	IOCHK-
RSTDRV	B2	A2	SD7
Vcc	B3	A3	SD6
IRQ9	B4	A4	SD5
-5V	B5	A5	SD4
DRQ2	B6	A6	SD3
-12V	B7	A7	SD2
0WS-	B8	A8	SD1
+12V	B9	A9	SD0
GND	B10	A10	IOCHRDY
SMEMW-	B11	A11	AEN
SMEMR-	B12	A12	SA19
IOW-	B13	A13	SA18
IOR-	B14	A14	SA17
DACK3-	B15	A15	SA16
DRQ3	B16	A16	SA15
DACK1-	B17	A17	SA14
DRQ1	B18	A18	SA13
REFRESH-	B19	A19	SA12
SYSCLK	B20	A20	SA11
IRQ7	B21	A21	SA10
IRQ6	B22	A22	SA9
IRQ5	B23	A23	SA8
IRQ4	B24	A24	SA7
IRQ3	B25	A25	SA6
DACK2-	B26	A26	SA5
TC	B27	A27	SA4
BALE	B28	A28	SA3
Vcc	B29	A29	SA2
OSC	B30	A30	SA1
GND	B31	A31	SA0
	KEY	KEY	
MEMCS16-	D1	C1	SBHE-
IOCS16-	D2	C2	LA23
IRQ10	D3	C3	LA22
IRQ11	D4	C4	LA21
IRQ12	D5	C5	LA20
IRQ15	D6	C6	LA19
IRQ14	D7	C7	LA18
DACK0-	D8	C8	LA17
DRQ0	D9	C9	MEMR-
DACK5-	D10	C10	MEMW-
DRQ5	D11	C11	SD8
DACK6-	D12	C12	SD9
DRQ6	D13	C13	SD10
DACK7-	D14	C14	SD11
DRQ7	D15	C15	SD12
Vcc	D16	C16	SD13
Master-	D17	C17	SD14
GND	D18	C18	SD15

PCI CONNECTORS - J4H1,J5H1,J5H2

Signal Name	*Pin*	*Pin*	*Signal Name*
TRST-	A1	B1	-12V
+12V	A2	B2	TCK
TMS	A3	B3	GND
TDI	A4	B4	TD0
+5V	A5	B5	+5V
IRQA-	A6	B6	+5V
IRQC-	A7	B7	IRQB-
+5V	A8	B8	IRQD-
Reserved	A9	B9	PRSNT1-
+5V	A10	B10	Reserved
Reserved	A11	B11	PRSNT2-
GND	A12	B12	GND
GND	A13	B13	GND
Reserved	A14	B14	Reserved
RESET	A15	B15	GND
+5V	A16	B16	CLK
GNT-	A17	B17	GND
GND	A18	B18	REQ-
Reserved	A19	B19	+5V
AD30	A20	B20	AD31
3.3V	A21	B21	AD29
AD28	A22	B22	GND
AD26	A23	B23	AD27
GND	A24	B24	AD25
AD24	A25	B25	3.3V
IDSEL	A26	B26	C/BE3-
3.3V	A27	B27	AD23
AD22	A28	B28	GND
AD20	A29	B29	AD21
GND	A30	B30	AD19
AD18	A31	B31	3.3V

Signal Name	*Pin*	*Pin*	*Signal Name*
AD16	A32	B32	AD17
3.3V	A33	B33	C/BE2-
FRAME-	A34	B34	GND
GND	A35	B35	IRDY-
TRDY-	A36	B36	3.3V
GND	A37	B37	DEVSEL-
STOP-	A38	B38	GND
3.3V	A39	B39	LOCK-
SDONE	A40	B40	PERR-
SBO-	A41	B41	3.3V
GND	A42	B42	SERR-
PAR	A43	B43	3.3V
AD15	A44	B44	C/BE1-
3.3V	A45	B45	AD14
AD13	A46	B46	GND
AD11	A47	B47	AD12
GND	A48	B48	AD10
AD9	A49	B49	GND
KEY	A50	B50	KEY
KEY	A51	B51	KEY
C/BEO-	A52	B52	AD8
3.3V	A53	B53	AD7
AD6	A54	B54	3.3V
AD4	A55	B55	AD5
GND	A56	B56	AD3
AD2	A57	B57	GND
AD0	A58	B58	AD01
+5V	A59	B59	+5V
REQ64-	A60	B60	ACK64-
+5V	A61	B61	+5V
+5V	A62	B62	+5V